

Gate Dielectric Metrology Using Advanced TEM Techniques

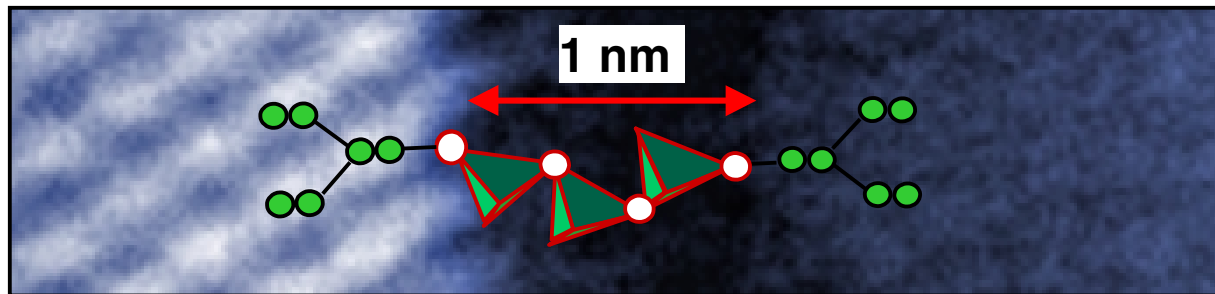
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Frieder Baumann, Tom Sorsch, Steve Moccio
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Bell Labs, Lucent Technologies

Jeff Neaton,

Physics Dept, Cornell University



silicon

SiO₂

polysilicon

Nature, **399**, 758 (1999), *Phys. Rev. Lett.* (Aug 3)

Lucent Technologies
Bell Labs Innovations



When do we stop?

Reliability: ~~25~~ ~~22~~ ~~18~~ 16 Å

processing and yield issue

Tunneling : 15 Å

Design Issue: chosen for 1A/cm² leakage
 $I_{on}/I_{off} \gg 1$ at 12 Å

Bonding:

Fundamental Issue- how many atoms

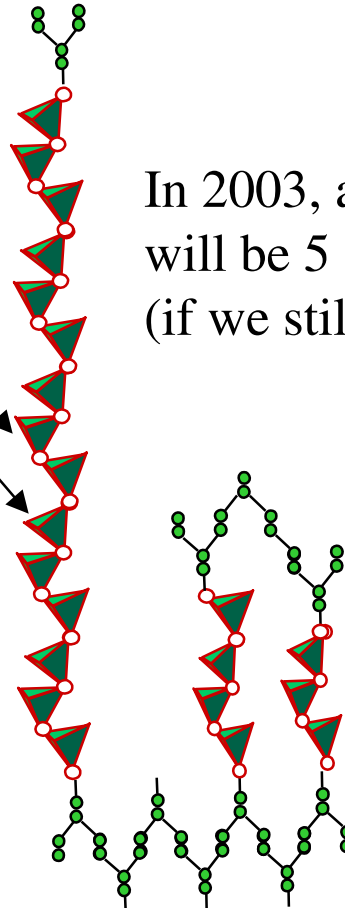
- do we need to get bulk-like properties?
- Is the interface electronically abrupt?
- Can we control roughness?

In 1997, a gate oxide was 15 silicon atoms thick.

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In 2003, a gate oxide will be 5 silicon atoms thick (if we still use SiO₂),

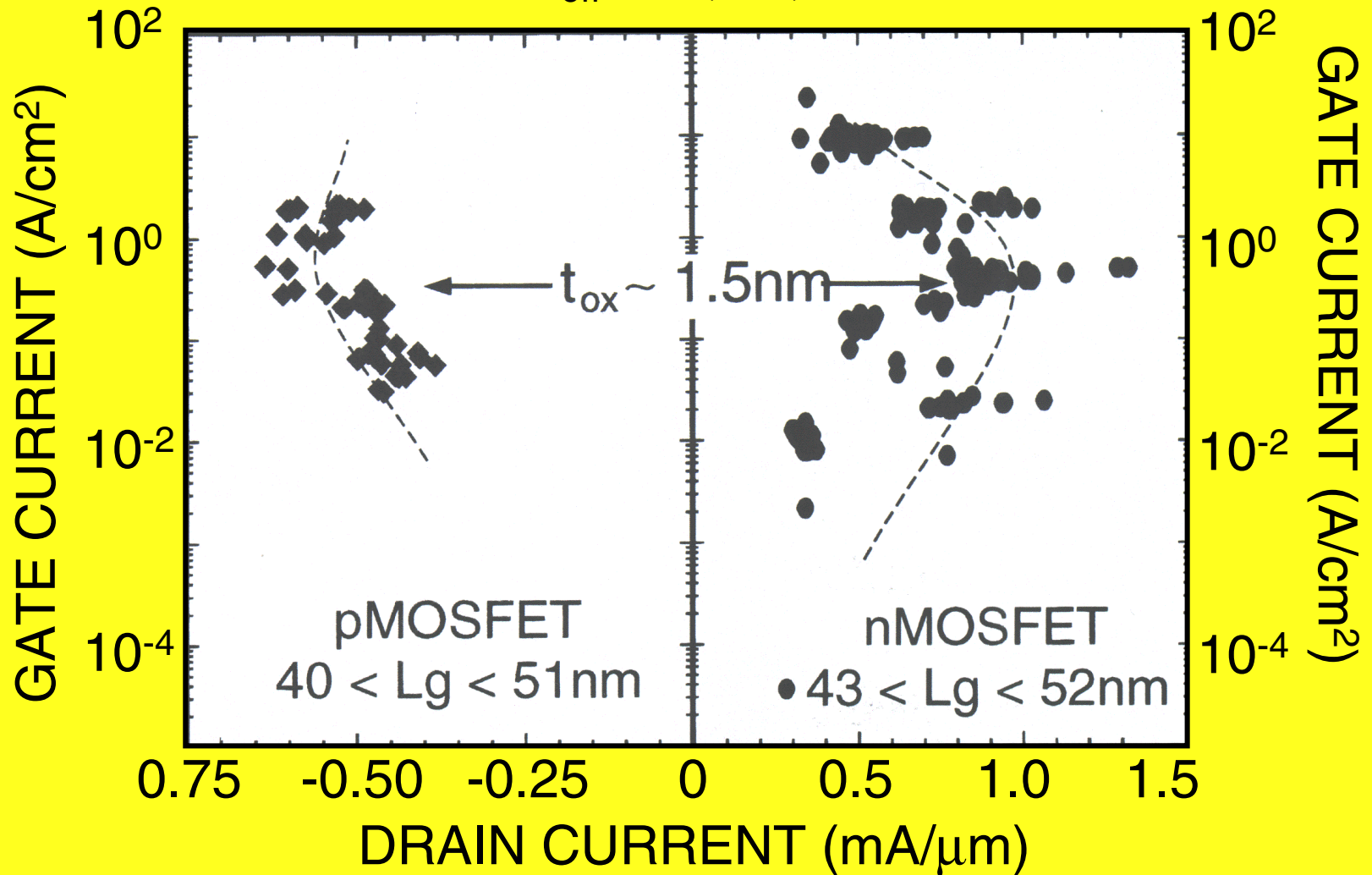
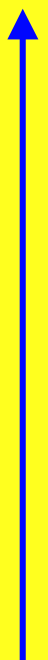
and at least 2 of those 5 atoms will be at the interfaces.



DC Performance / Gate Leakage Current Density

($I_{\text{off}} < 2 \mu\text{A}/\mu\text{m}$)

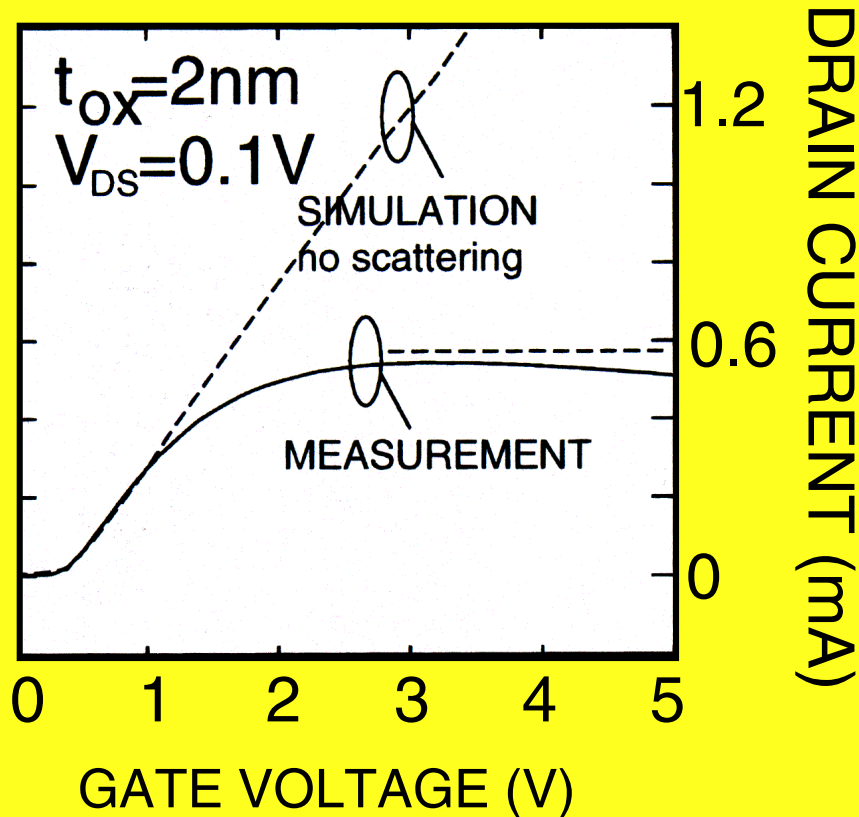
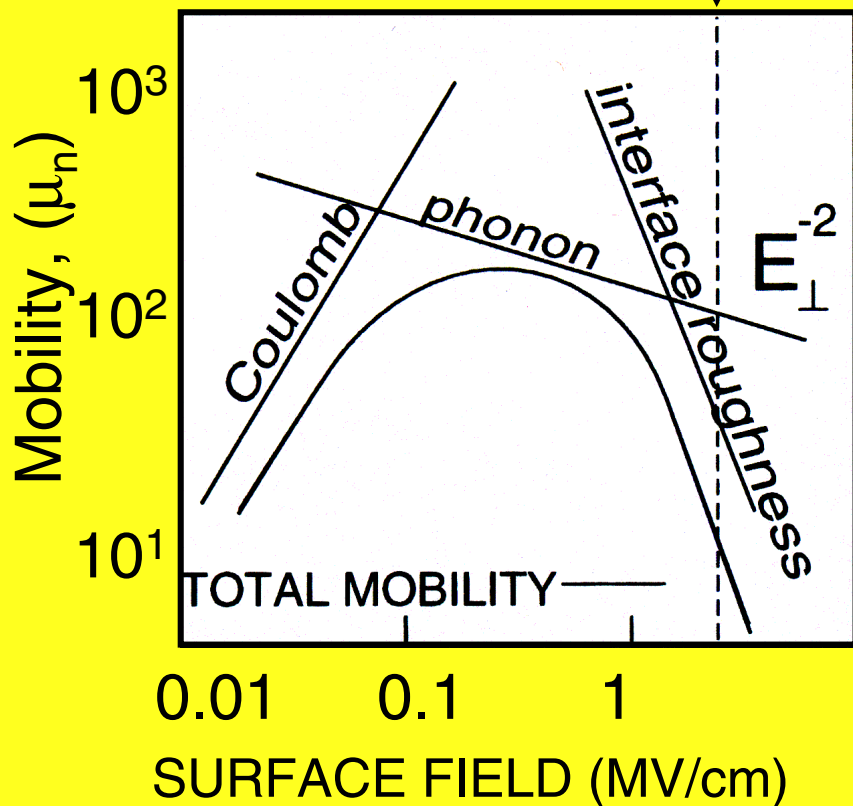
Decreasing Thickness



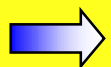
While gate current deteriorates,
drive current does not improve for $t_{\text{ox}} < 1.3 \text{ nm}$

Interface Roughness Scattering in MOSFET

50 nm

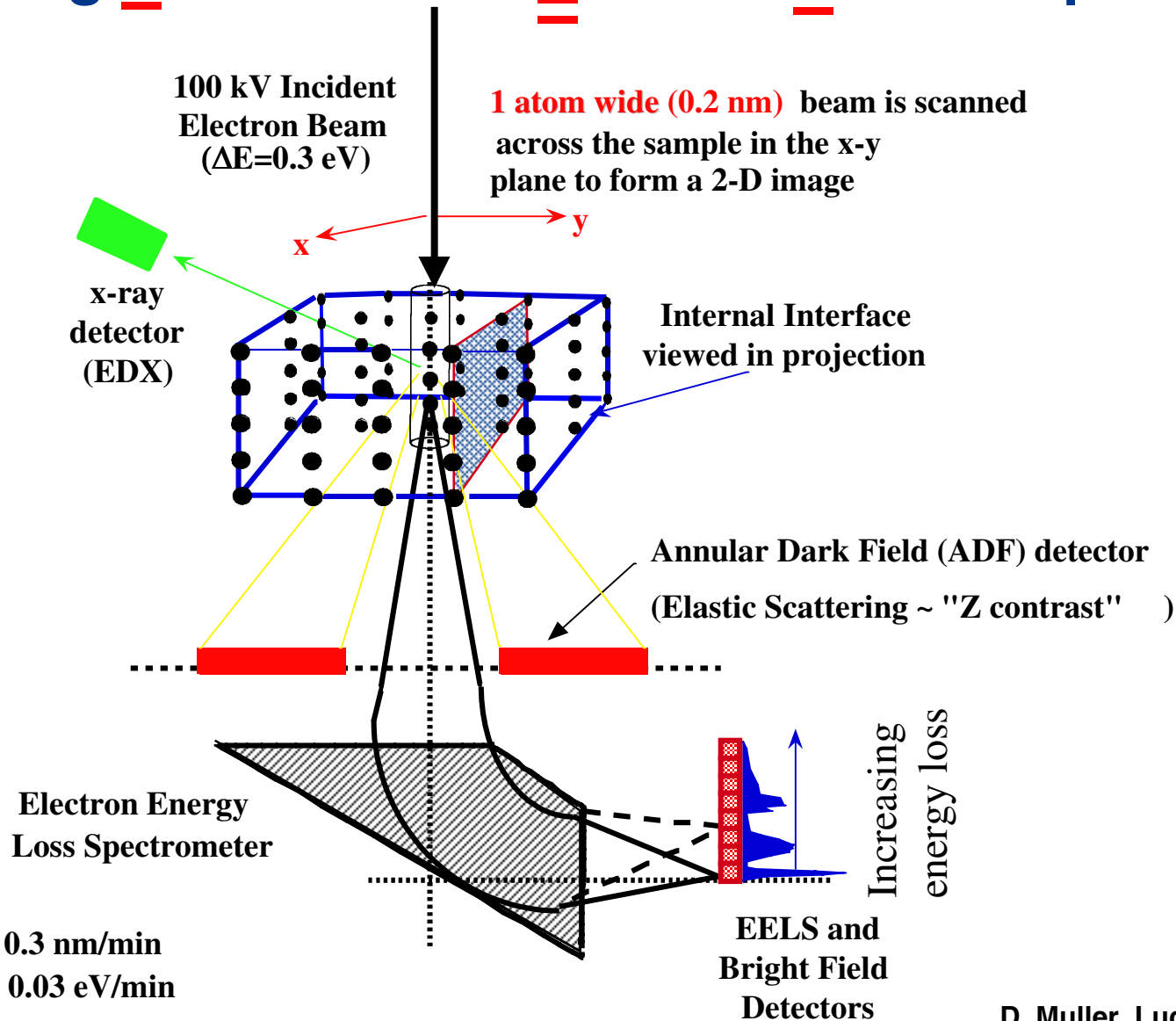


Surface scattering predominates for $E_{\perp} > 0.5$ MV/cm

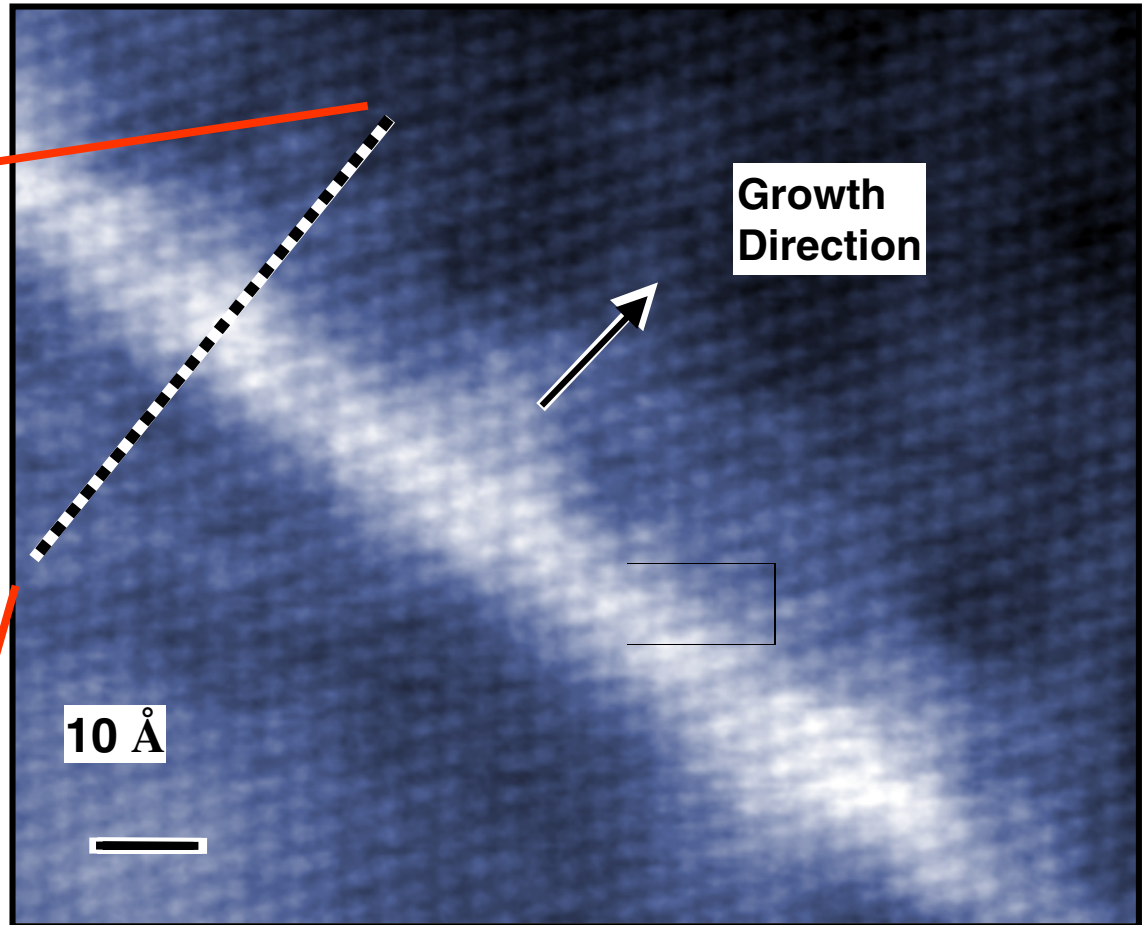
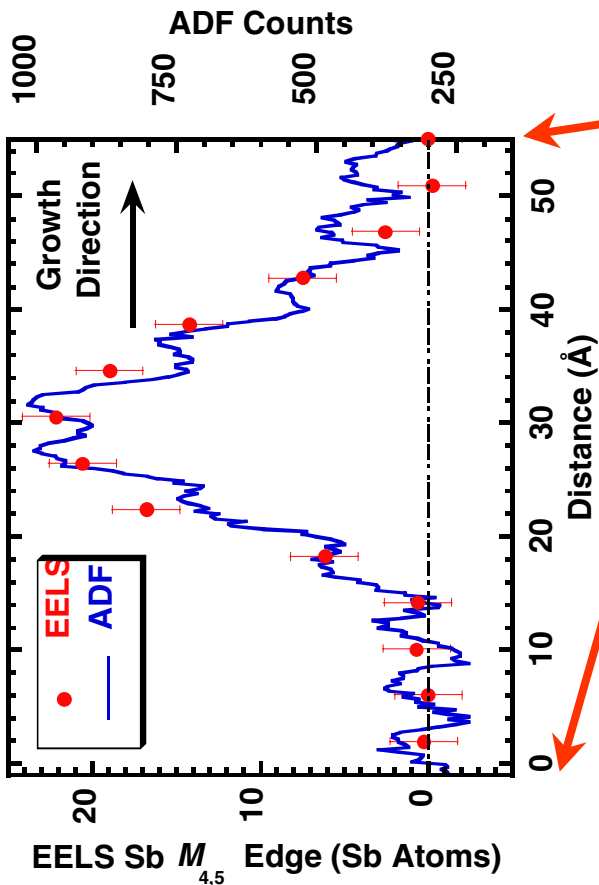


Need to reduce roughness (even for High k)

Spatially Resolved Electron Energy Loss Spectroscopy in a Field Emission Scanning Transmission Electron Microscope



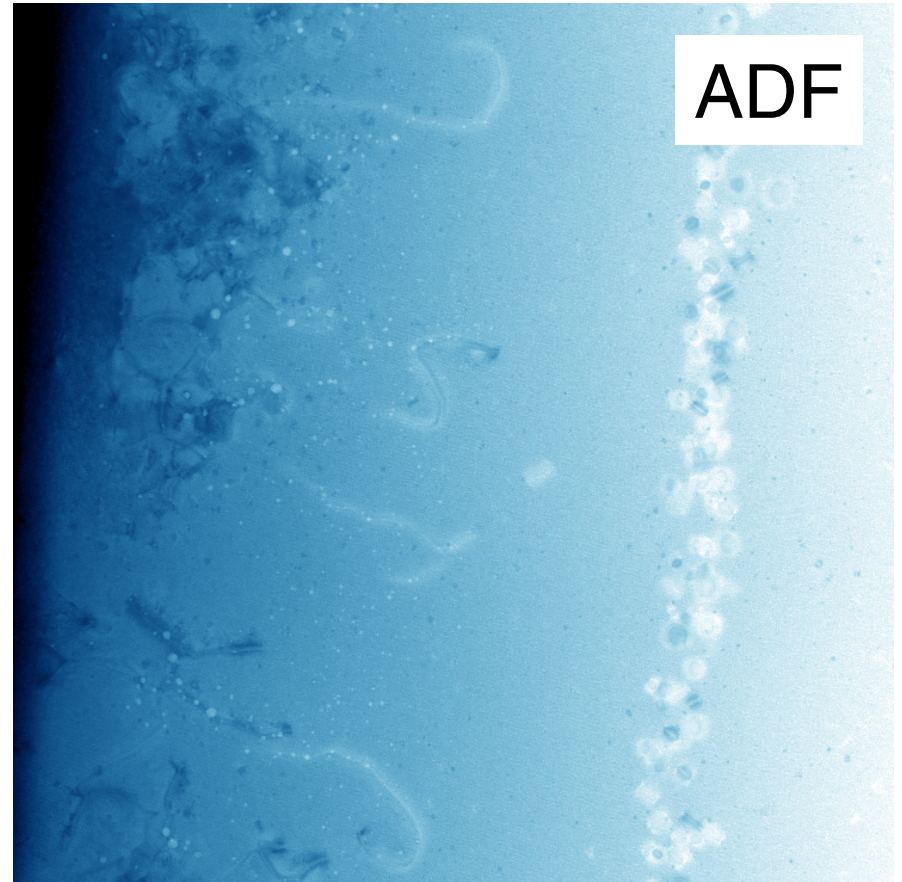
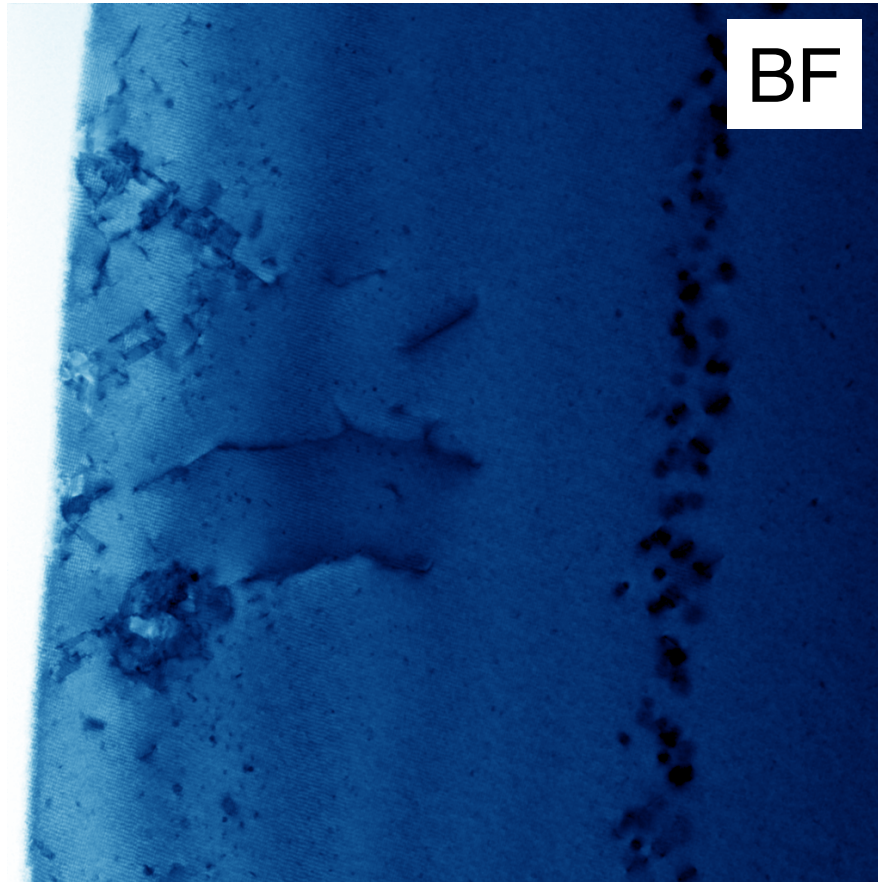
2D-Dopant Profiling in Silicon: Imaging Sb Atoms in a δ -doped layer



Detection Limit is ± 2 atoms by EELS, ± 0.5 atoms by ADF



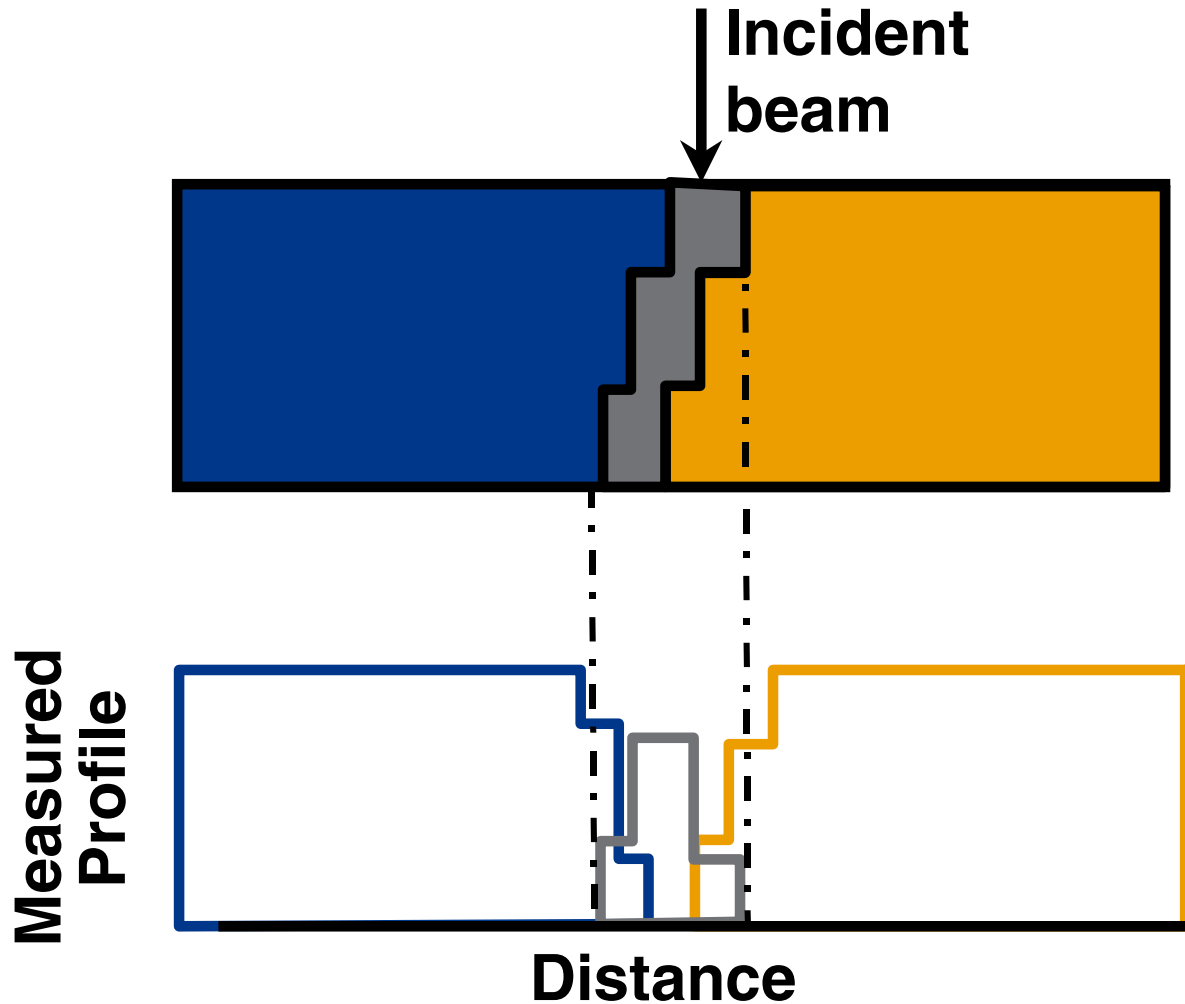
ADF is Not Always “Z-Contrast” (Hf Implants in Si)



BF: Thickness+diffraction contrast

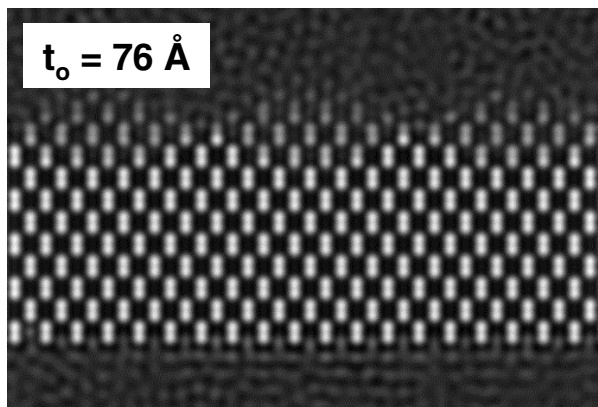
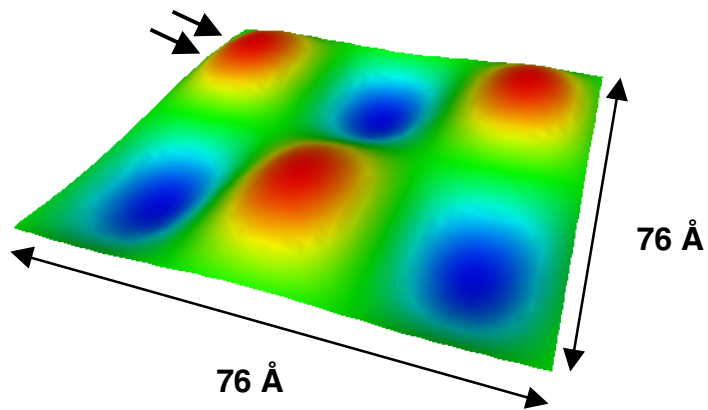
ADF: strain contrast from dechanneling in thick samples

A Problem with Roughness: The interface is viewed in projection



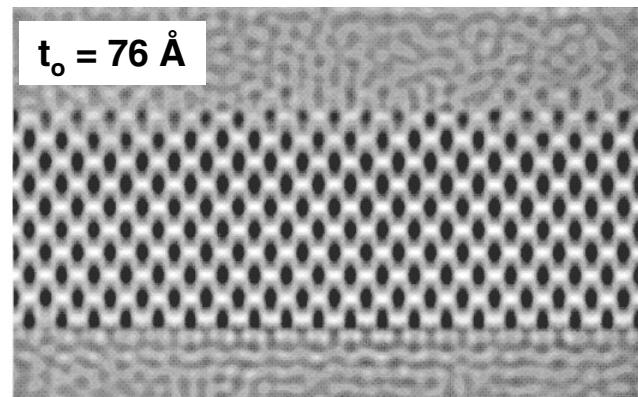
Roughness increases the apparent width of the interfacial region

Imaging Interface Roughness in Projection



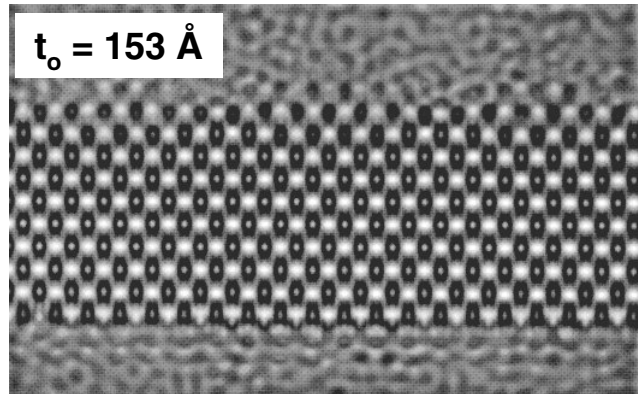
ADF-STEM

CTEM or BF-STEM



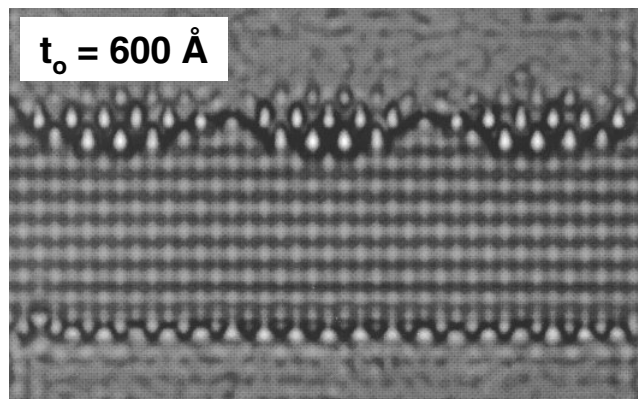
Rough interface

Smooth interface



Rough interface

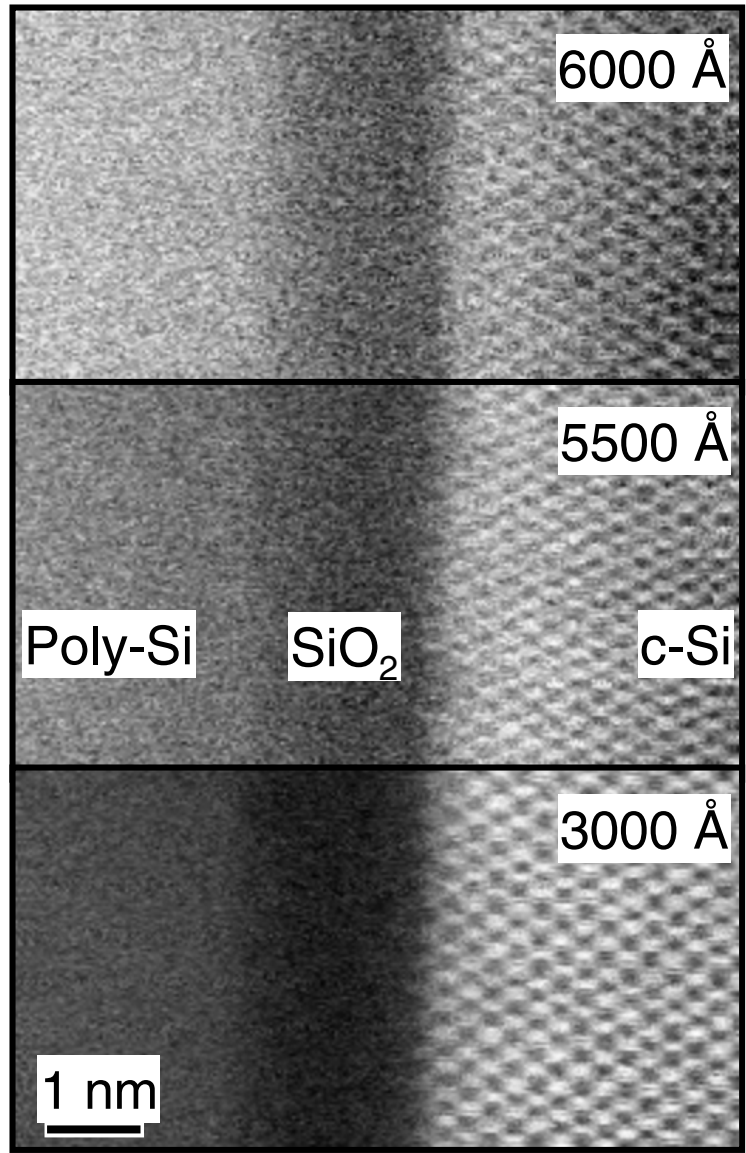
Smooth interface



Rough interface

Smooth interface

Imaging Thick Cross-Sections



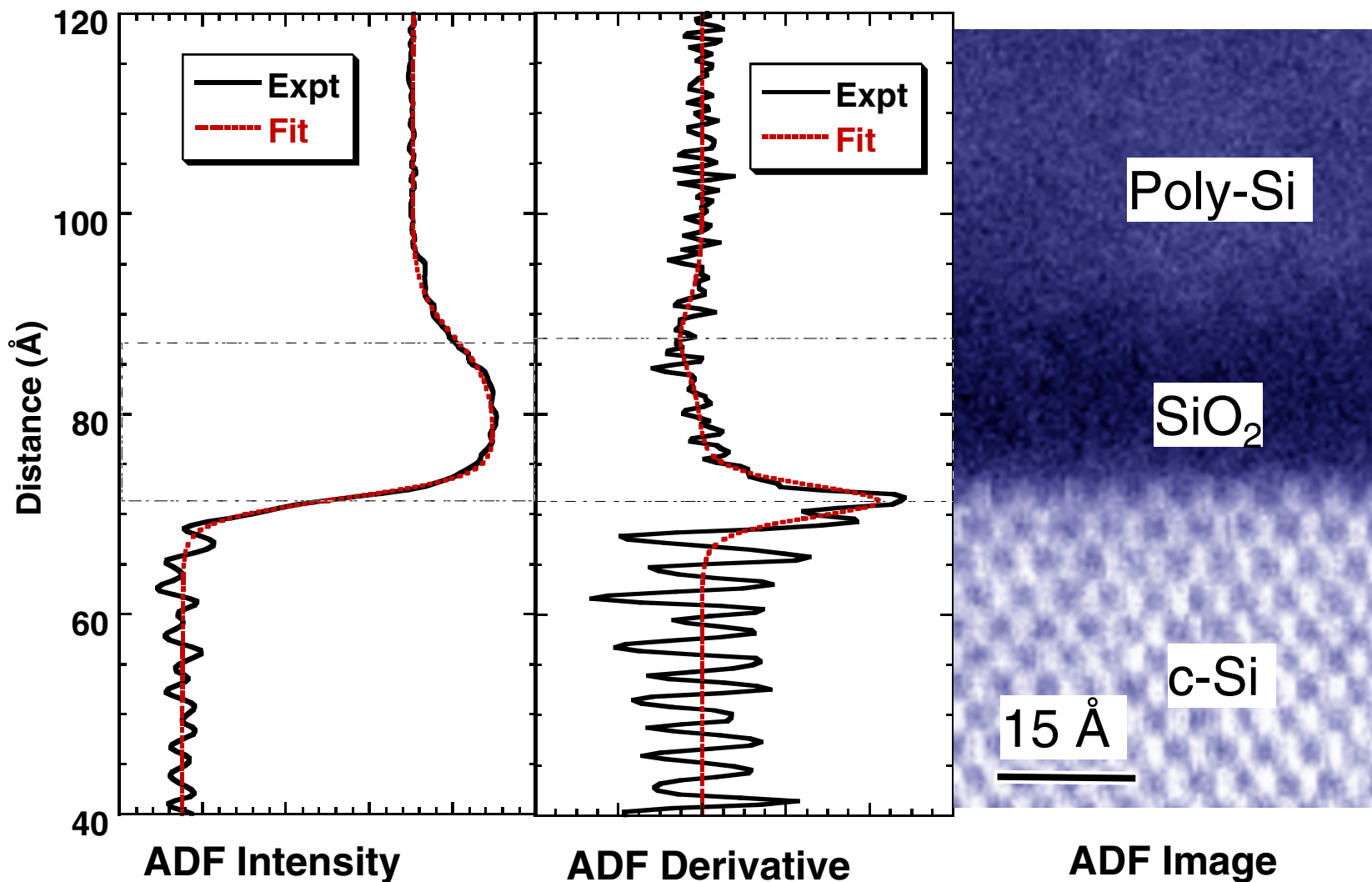
- ADF Images decay gracefully with increasing thickness
- Apparent Oxide Thickness is unchanged with thickness
- Apparent Interface Roughness increases from 1.6 to 2.7 Å rms
- “white band” develops (depends on thickness and ADF angles)

Gate Oxide Thickness: 20 Å

Measuring Projected Roughness



(180 nm Gate Length Transistor)

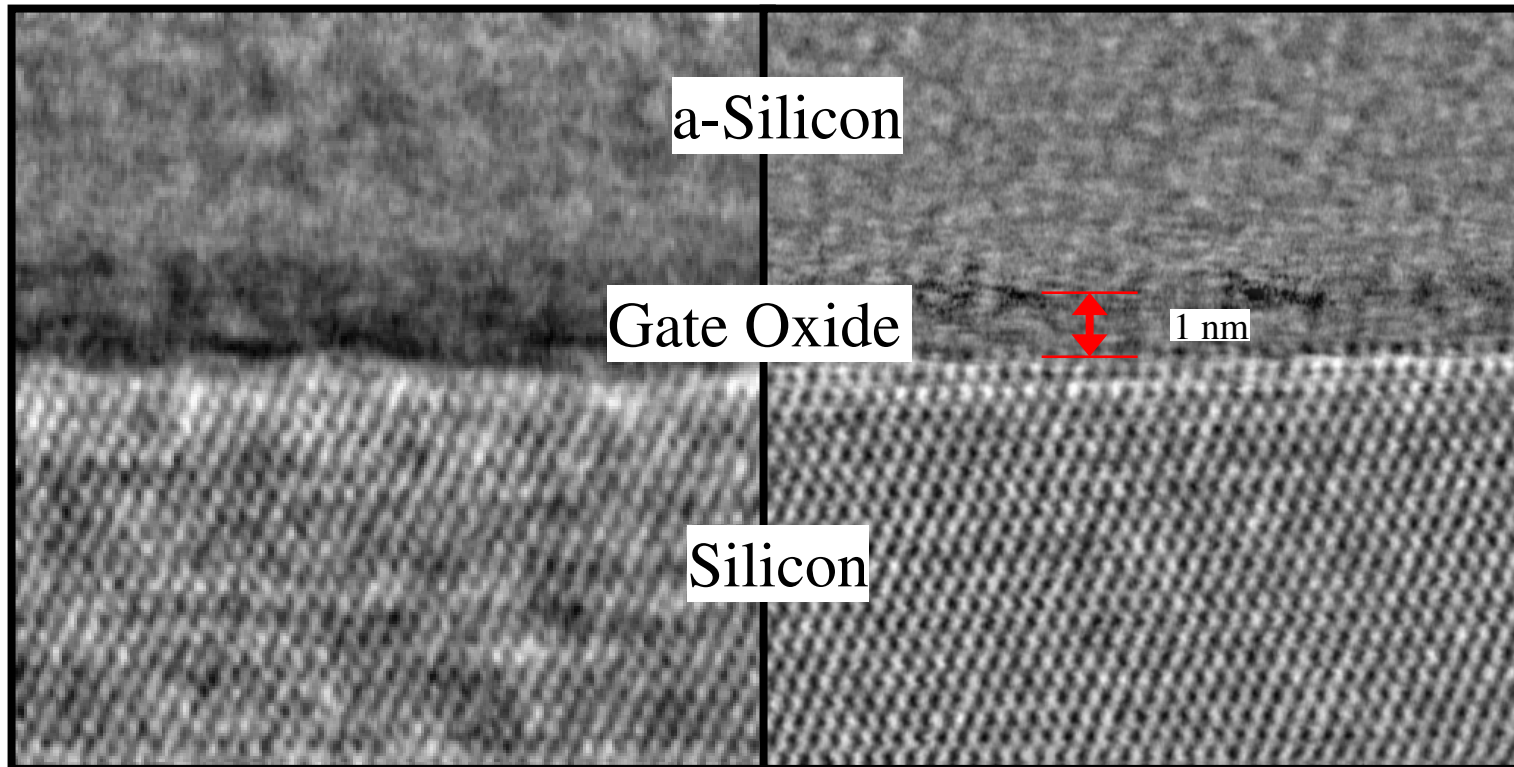


Gate Oxide Thickness: 15.3 Å, Si/SiO₂ Roughness $\sigma < 1.56$ Å

Imaging atomic scale Roughness



(Filtered ADF-STEM Images of 1 nm Gate Oxides)



Standard Clean

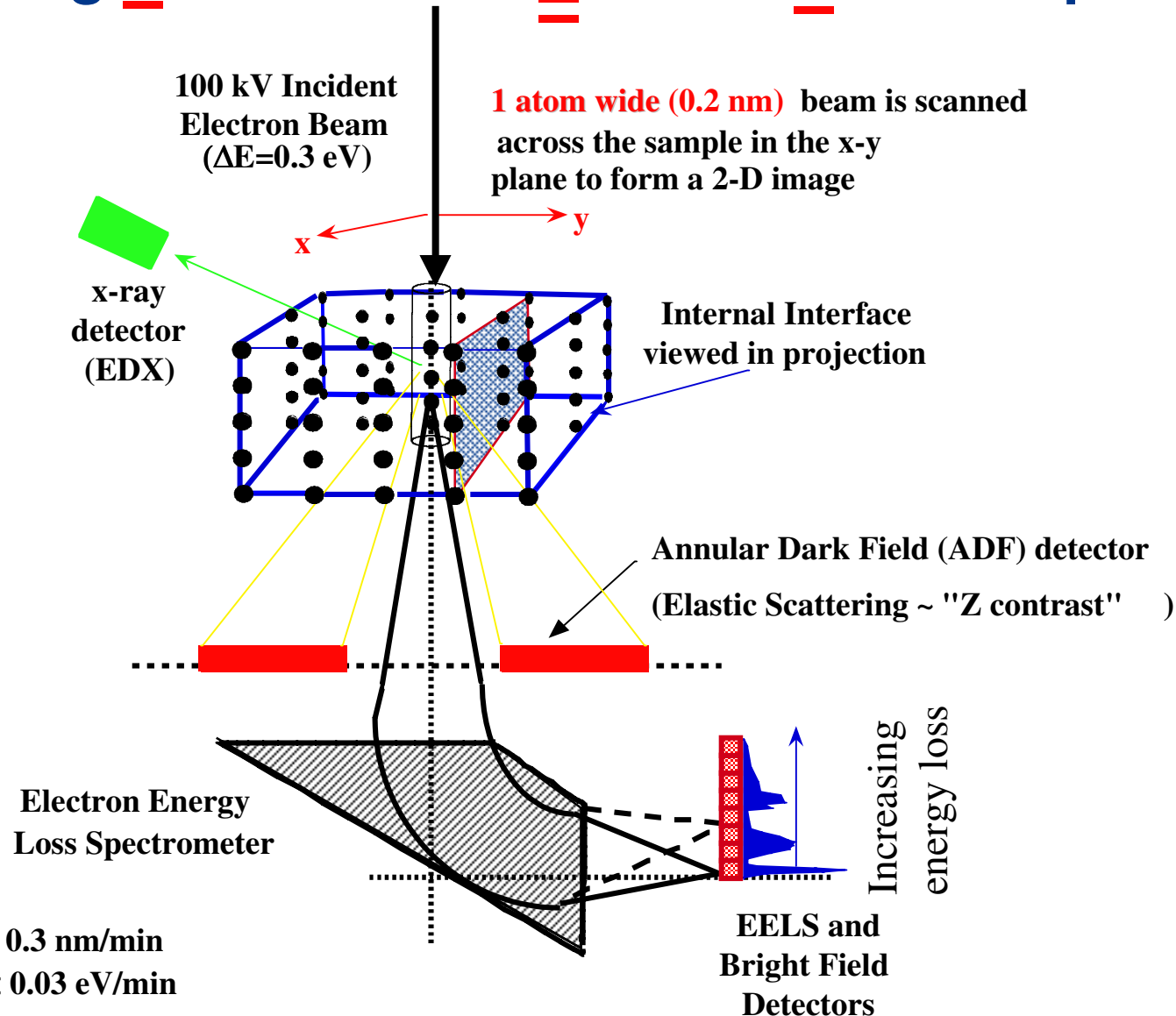
5.4 Å Peak-Peak Roughness
2.3 Å rms roughness (ADF)
2.2 Å rms roughness (xray)
(1.1 Å rms by AFM)

New Surface Preparation

1.8 Å Peak-Peak Roughness
1.25 Å rms roughness (ADF)
1.1 Å rms roughness (ADF)
0.5 Å rms by AFM

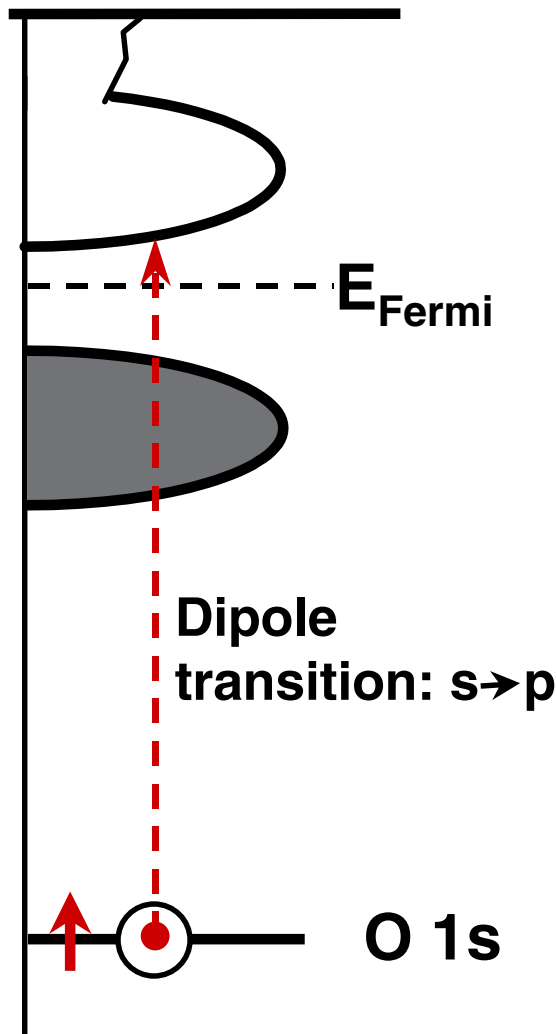


Spatially Resolved Electron Energy Loss Spectroscopy in a Field Emission Scanning Transmission Electron Microscope



Spatial Drift < 0.3 nm/min
Energy Drift < 0.03 eV/min

Core-Level Electron Energy Loss Spectroscopy



EELS measures a local density of states partitioned by

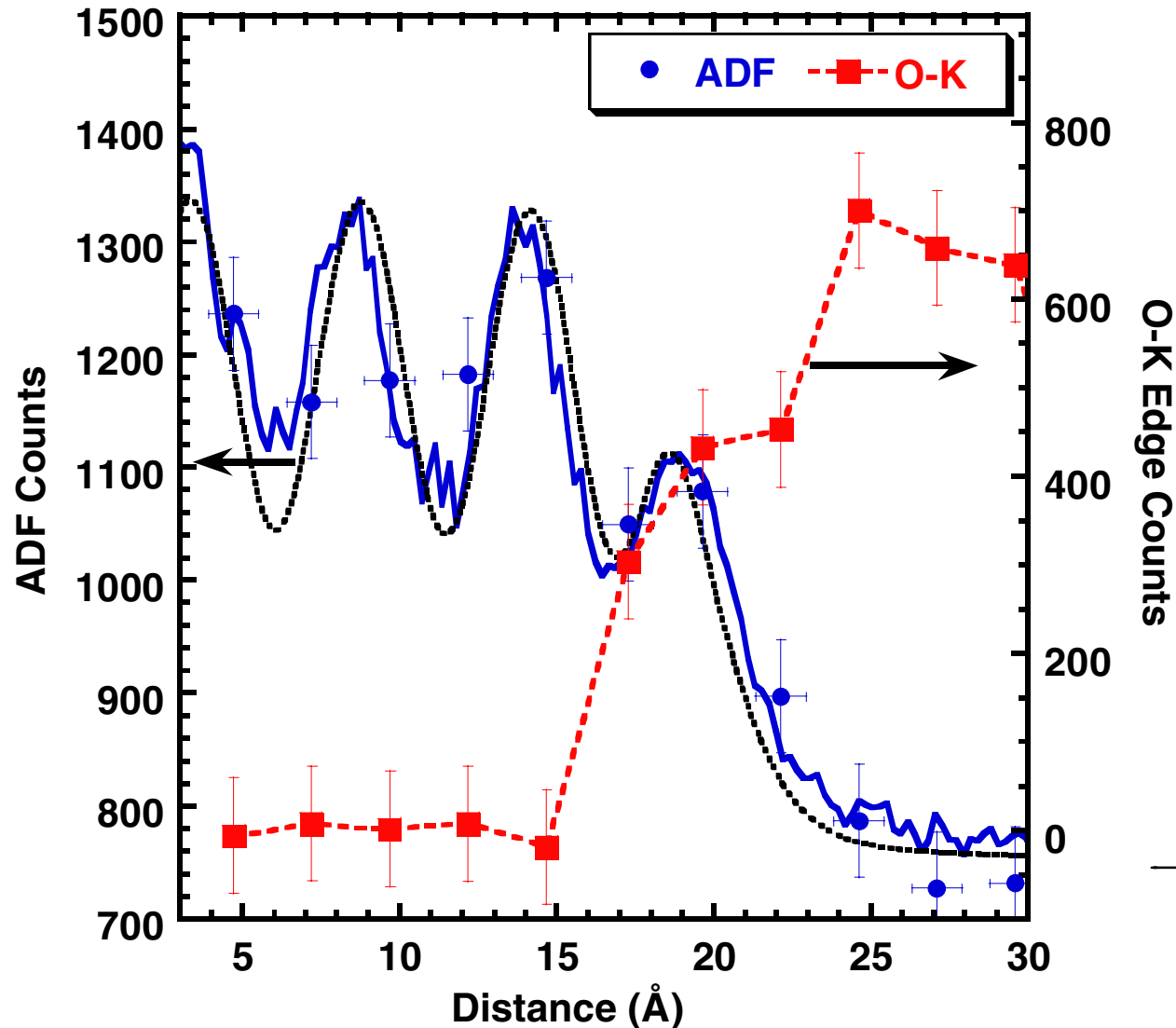
- site - as the probe is localized,
- element - the core level binding energy is unique
- angular momentum (s,p,d states separately)

Probes the conduction band and so provides local electronic information

O-K edge measures the empty O p-DOS which is sensitive to O-O 2nd neighbors (XPS is mostly nearest neighbors)

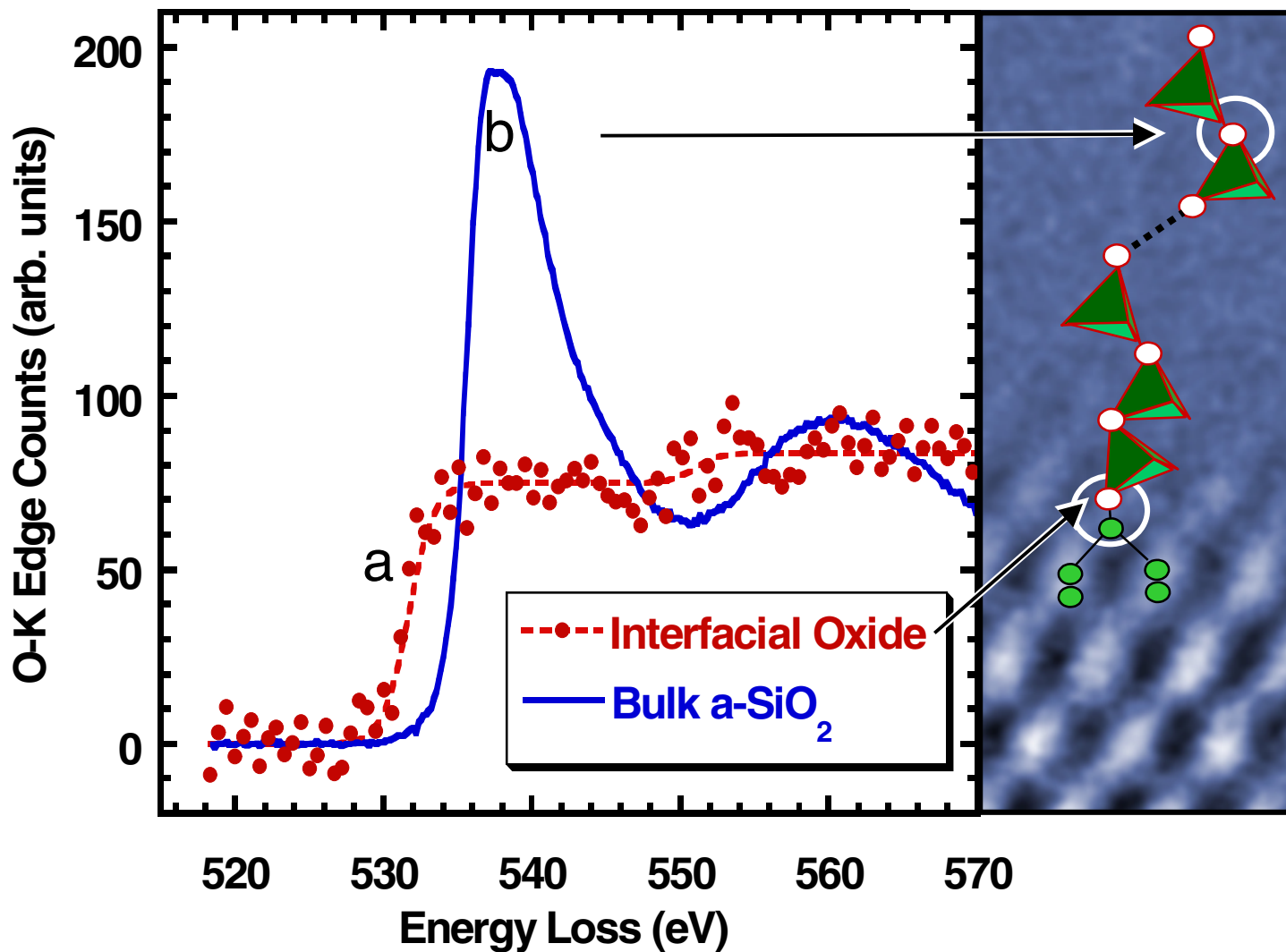
➡ EELS (and electrical) interface is wider than XPS (chemical)

Imaging the Si/SiO₂ Interface on an atomic scale requires a sharp interface





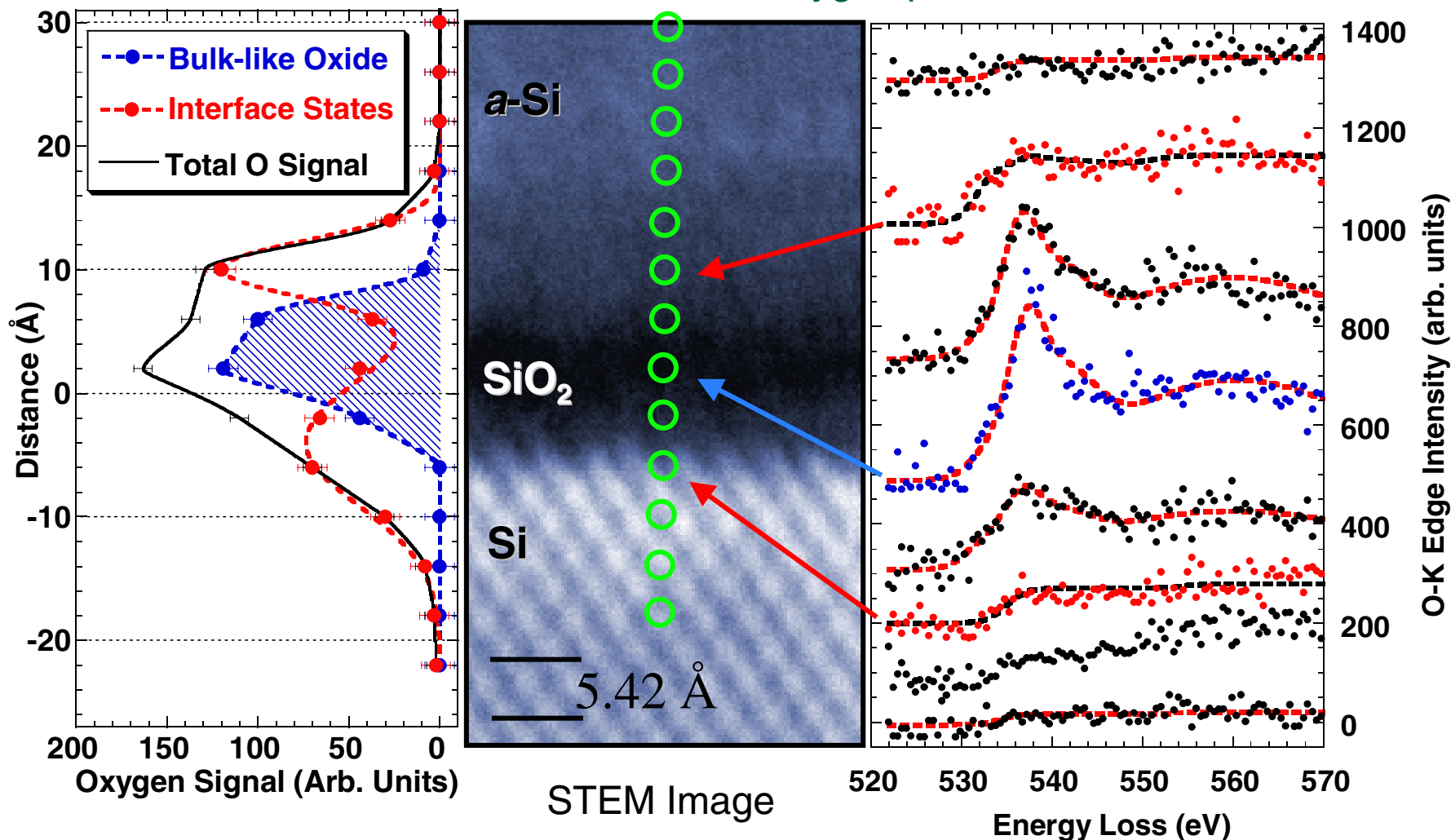
Oxygen Bonding at the Si/SiO₂ Interface



The edge onset is 3 eV lower at the interface

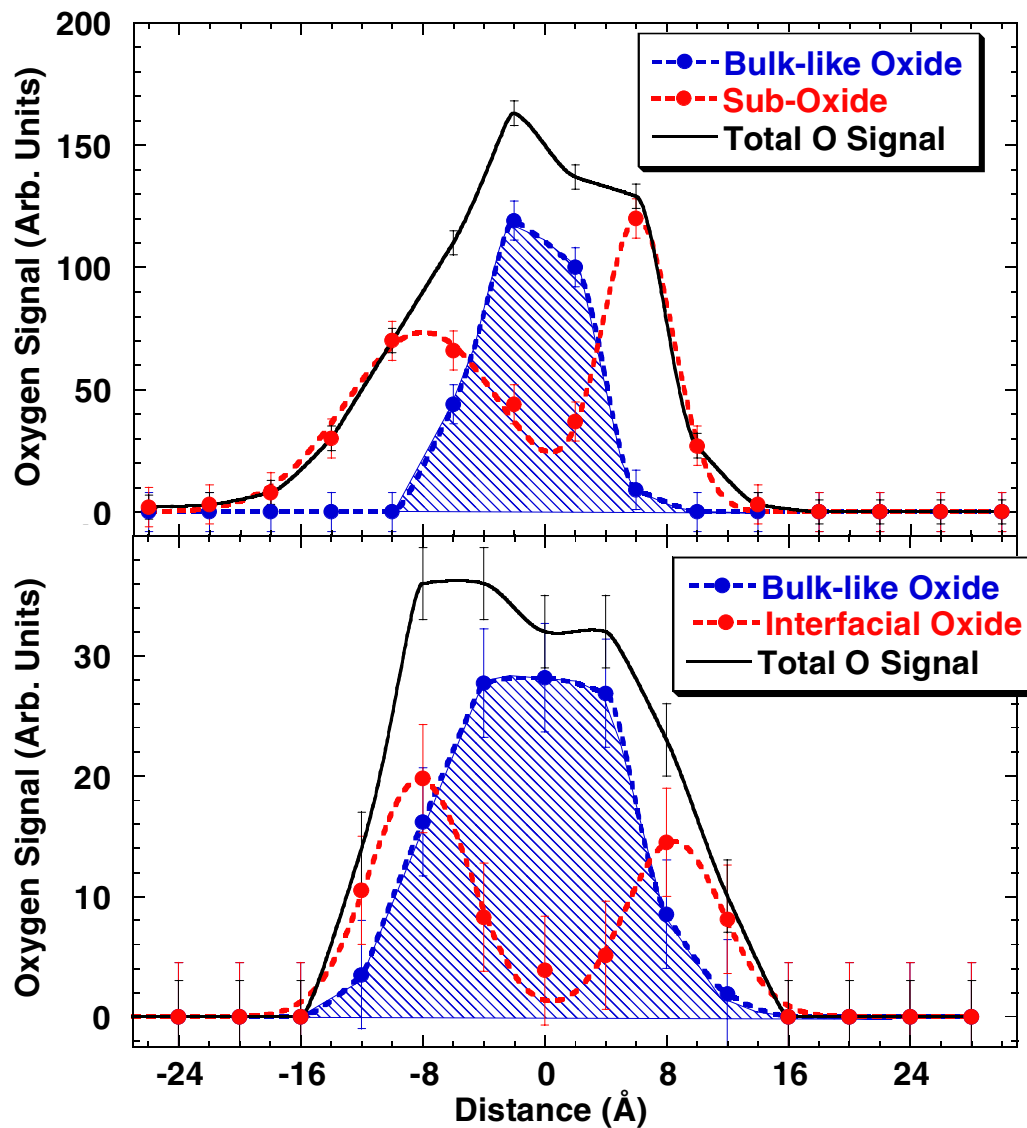
Oxygen Bonding from EELS (Chemistry on an Atomic Scale)

Nominal 1.1 nm SiO_2 : 0.8 - 1 nm Bulk SiO_2
1.6 nm wide oxygen profile





Oxygen Profile



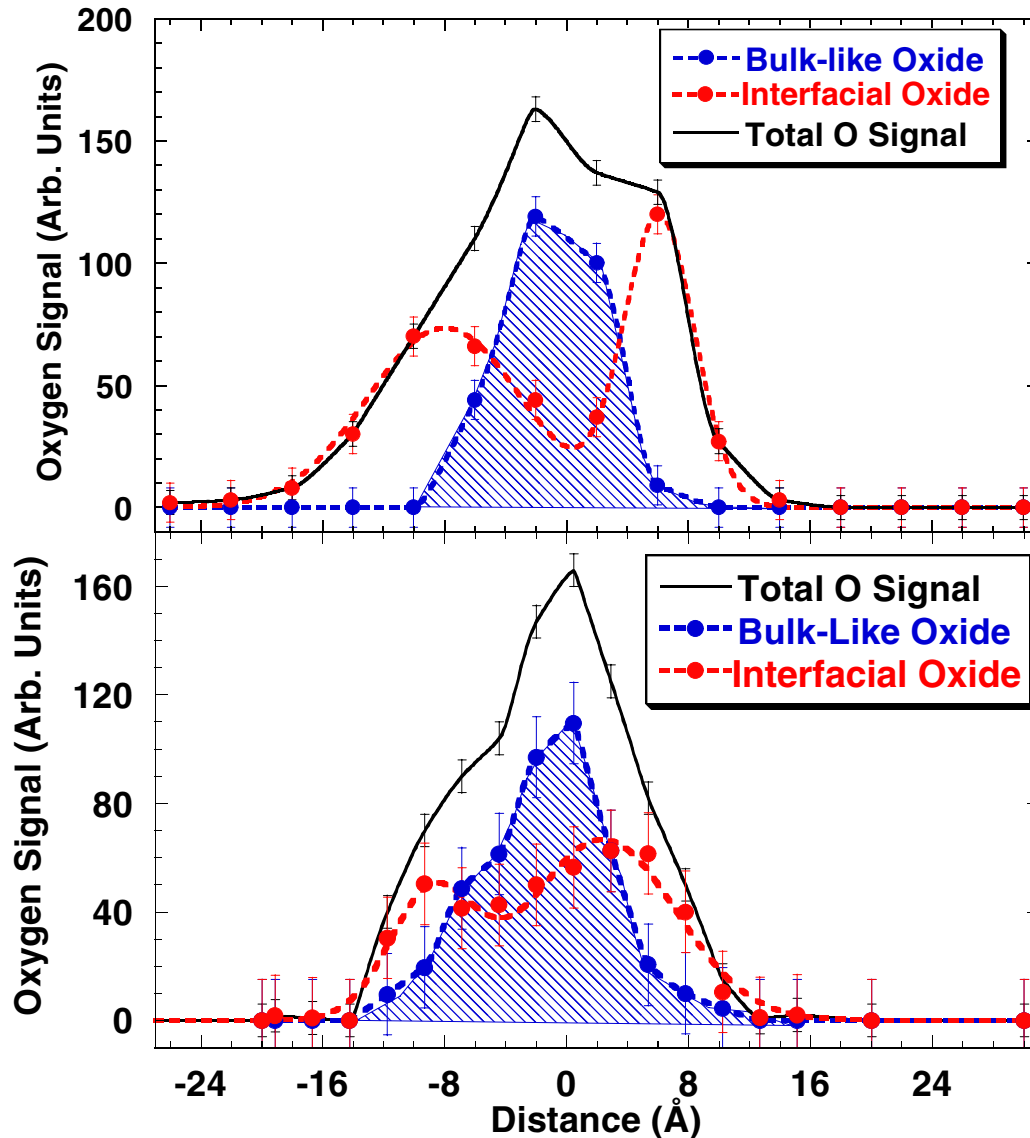
“10 Å” Gate Oxide :

- $60 \pm 5\%$ Interfacial Oxide
- Bulk-Like FWHM: 8.5 \AA
- Total Oxygen FWHM: 16 \AA

“18 Å” Gate Oxide :

- $35 \pm 5\%$ Interfacial Oxide
- Bulk-Like FWHM: 16 \AA
- Total Oxygen FWHM: 21 \AA

Oxygen Profile before and after annealing



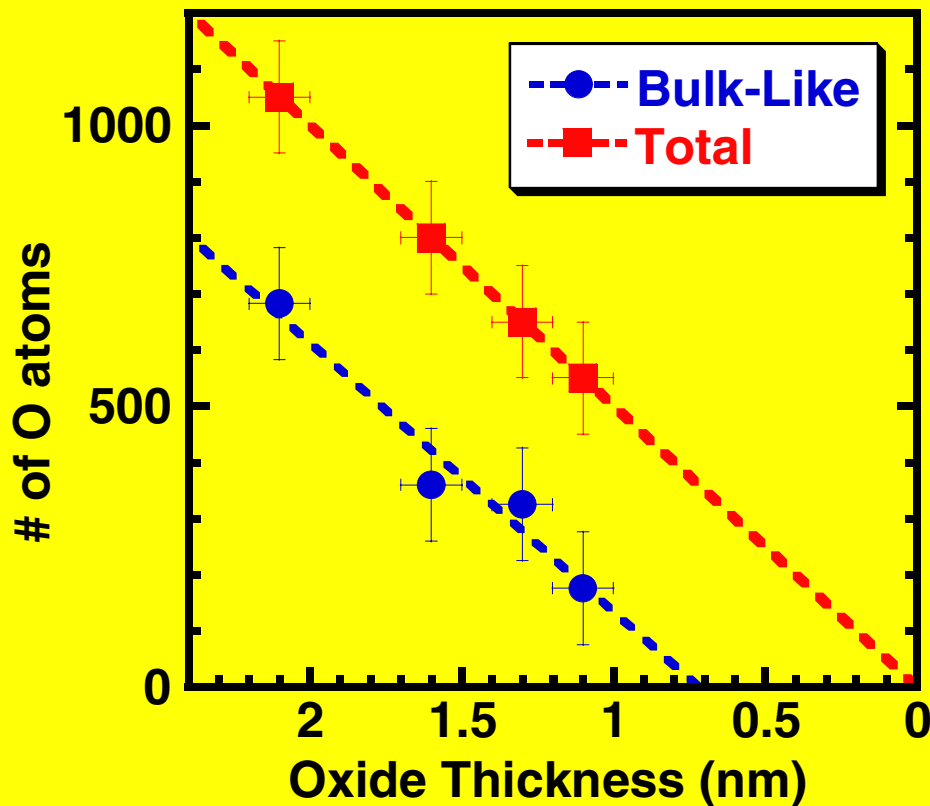
Before Annealing:

- $60 \pm 5\%$ Interfacial Oxide
- Bulk-Like FWHM: 8.5 \AA
- Total Oxygen FWHM: 16 \AA
- Upper interface is smoother than the lower.

After Annealing:

- $50 \pm 5\%$ Interfacial Oxide
- Bulk-Like FWHM: 8.5 \AA
- Total Oxygen FWHM: 13 \AA
- Lower interface unchanged
- Upper interface is now as rough as the lower.

The End of the Roadmap for SiO₂

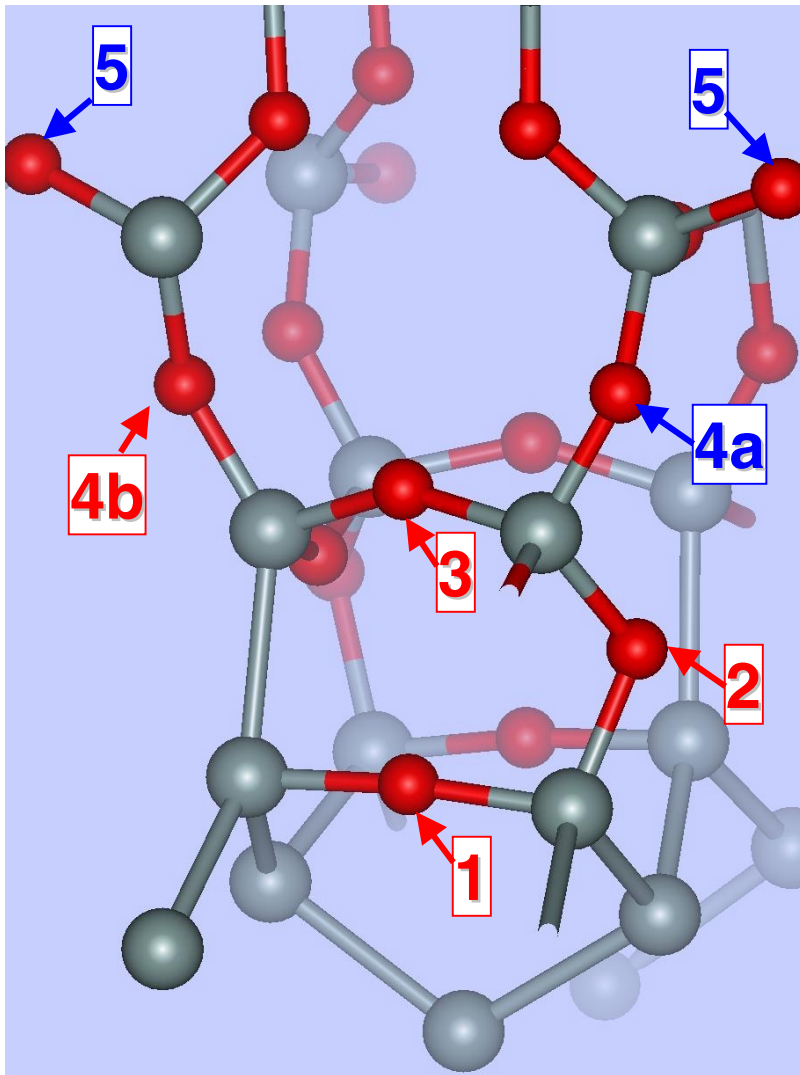


The Interface width is fixed

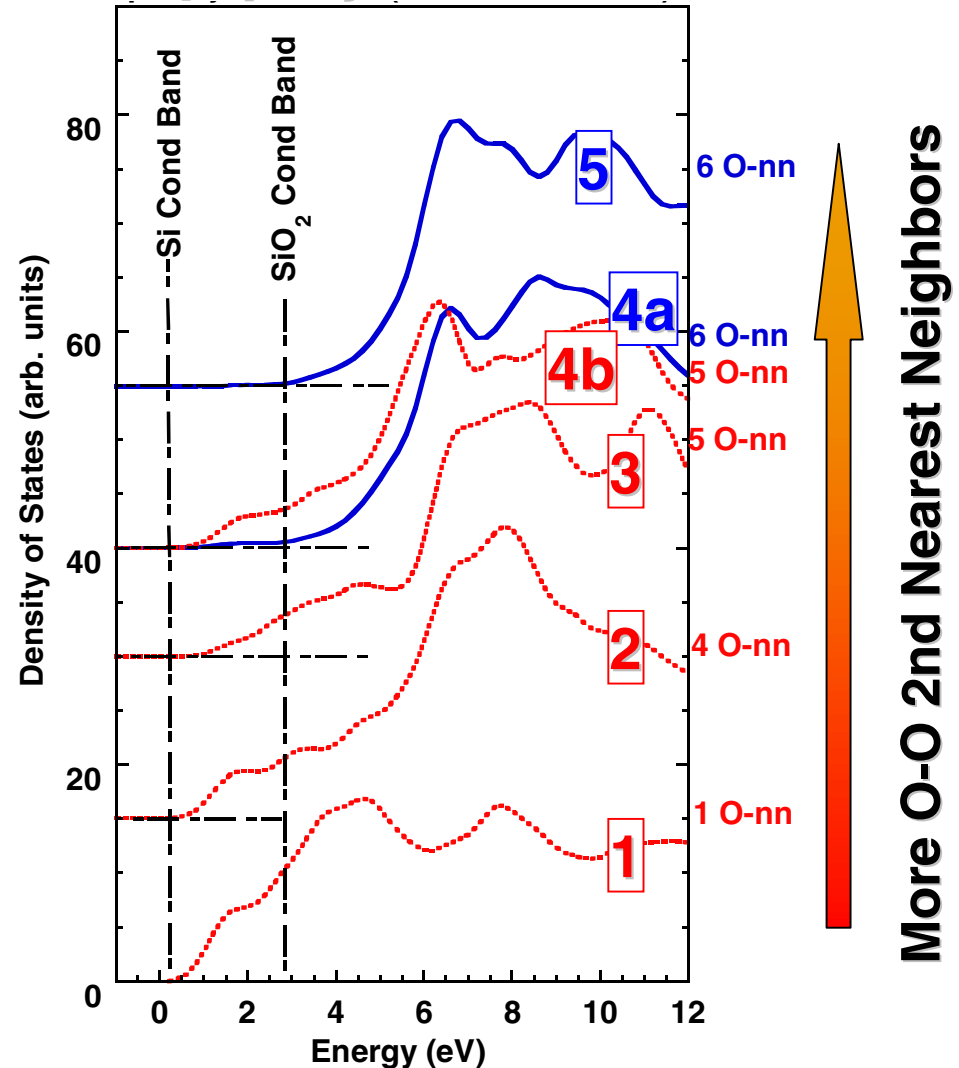


There will be no more Bulk-like bonding when the Oxide is less than 0.7 nm.

A Model Si/SiO₂ Interface



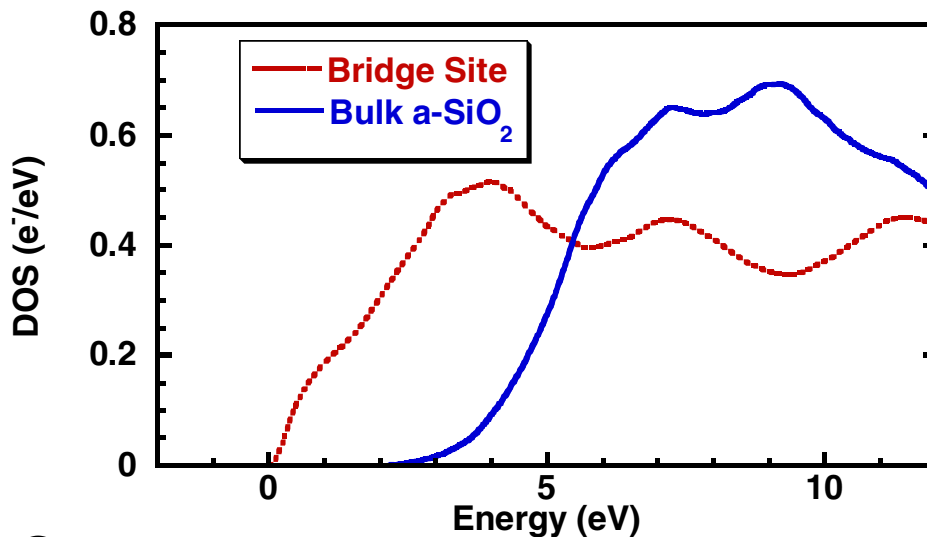
O *p*-projected DOS



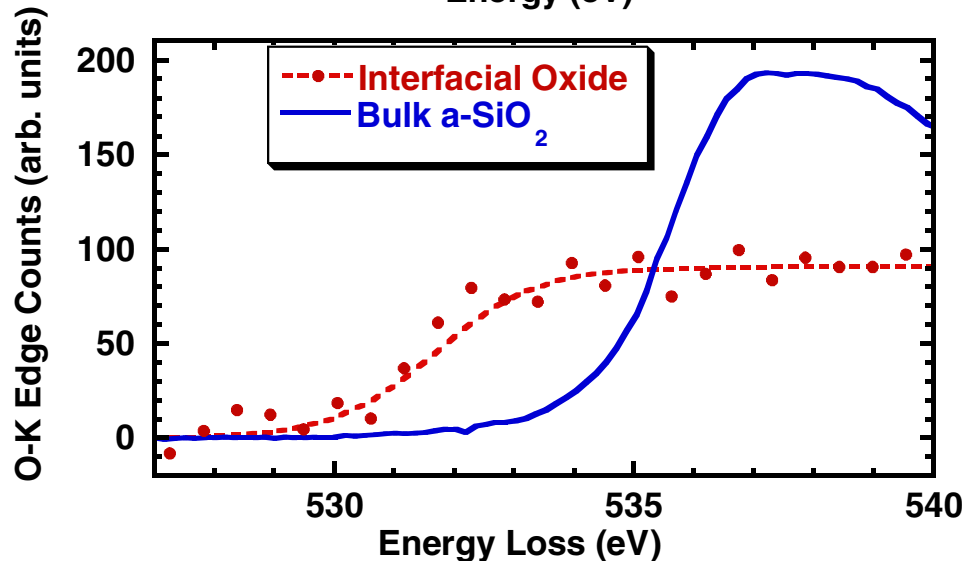
(Model-3 of A. Pasquarello, M. S. Hybertson, R. Car, PRB)



Oxygen Bonding at the Si/SiO₂ Interface



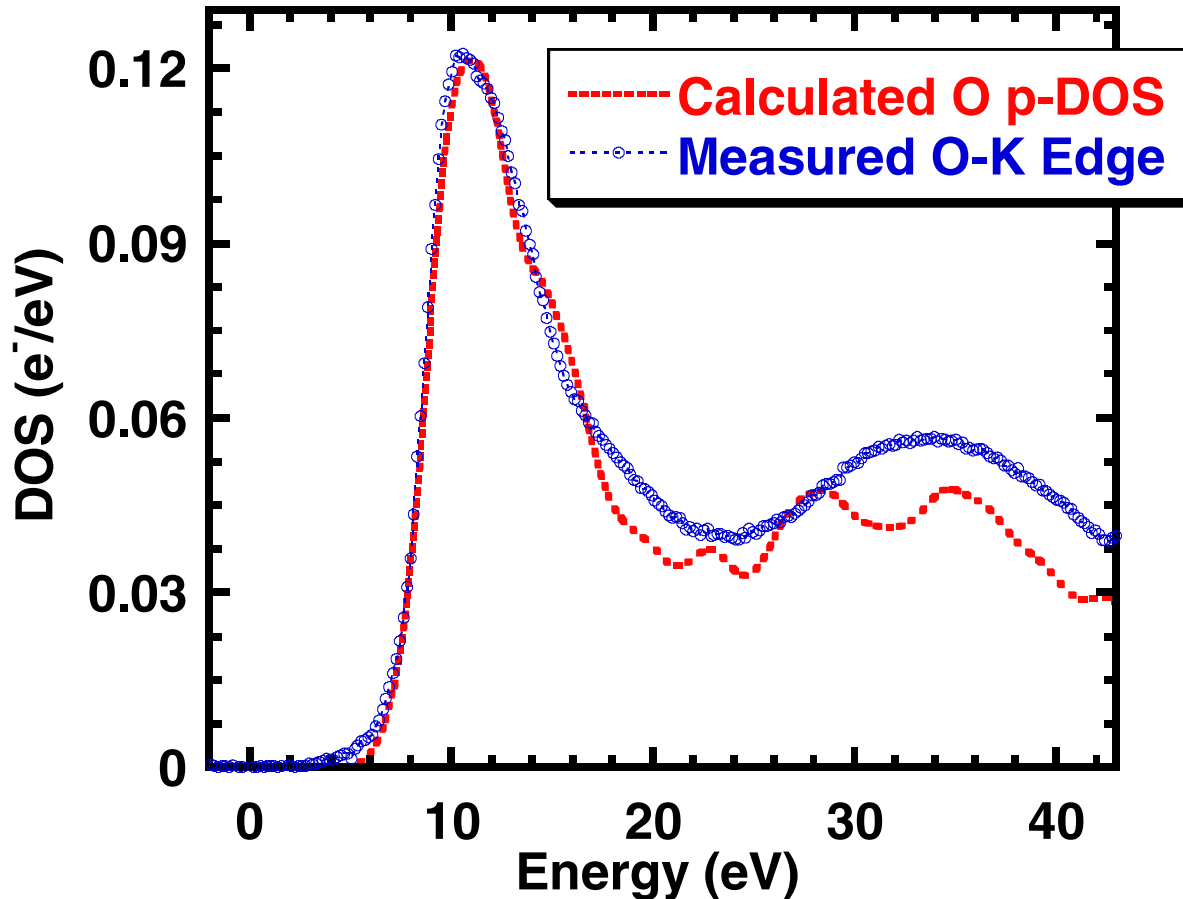
Theory



Experiment

The edge onset is 3 eV lower at the interface

Comparison of the Measured Oxygen-K Edge with *ab-initio* Calculations

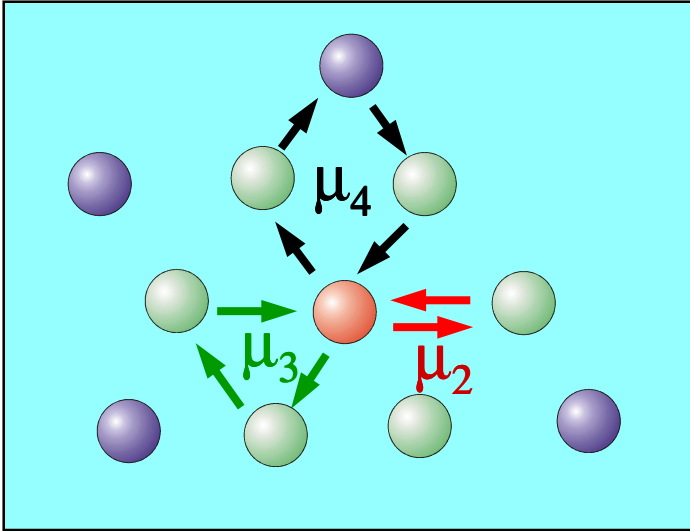


No strong core-hole effects on the oxygen-K Edge
(not true for the Si L edge)



Cyrot-Lackmann's Moments Theorem:

The shape of the local DOS depends on the local atomic arrangement:



2nd Moment: $\mu_2 = \sum_{\text{green}} \left(\frac{\beta}{\beta} \right) = z \beta^2$
 (Variance)

➔ Bandwidth, $W = \sqrt{\mu_2} = \sqrt{z} |\beta|$

➔ W depends only on neighbors

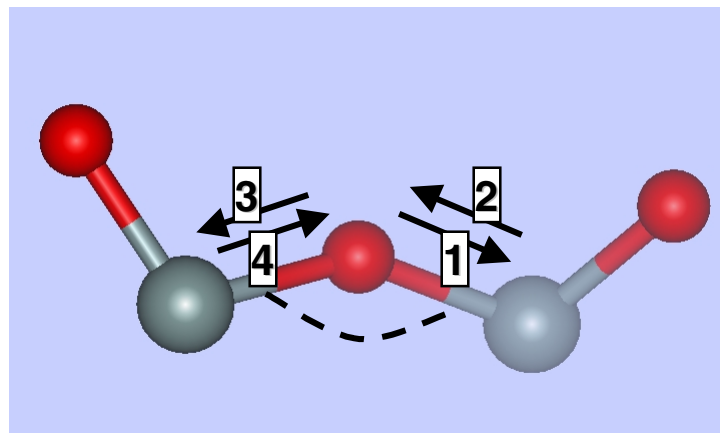
3rd Moment: $\mu_3 = \sum_{\text{green}} \sum_{\text{green}}$
 (Skewness)

4th Moment: $\mu_4 = \sum_{\text{green}} \sum_{\text{green}} \sum_{\text{purple}}$ $\gamma_4 = \mu_4 / (\mu_2)^2$
 (Kurtosis)



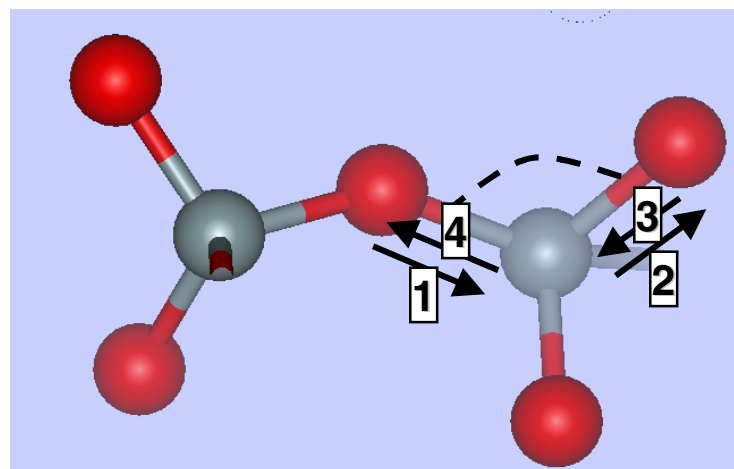
Fourth Moments in SiO_2

Nearest Neighbors
and
Si-O-Si Bond Angle



Small:
• 2 terms
• Node on O

Second Neighbors
and
O-Si-O Bond Angle



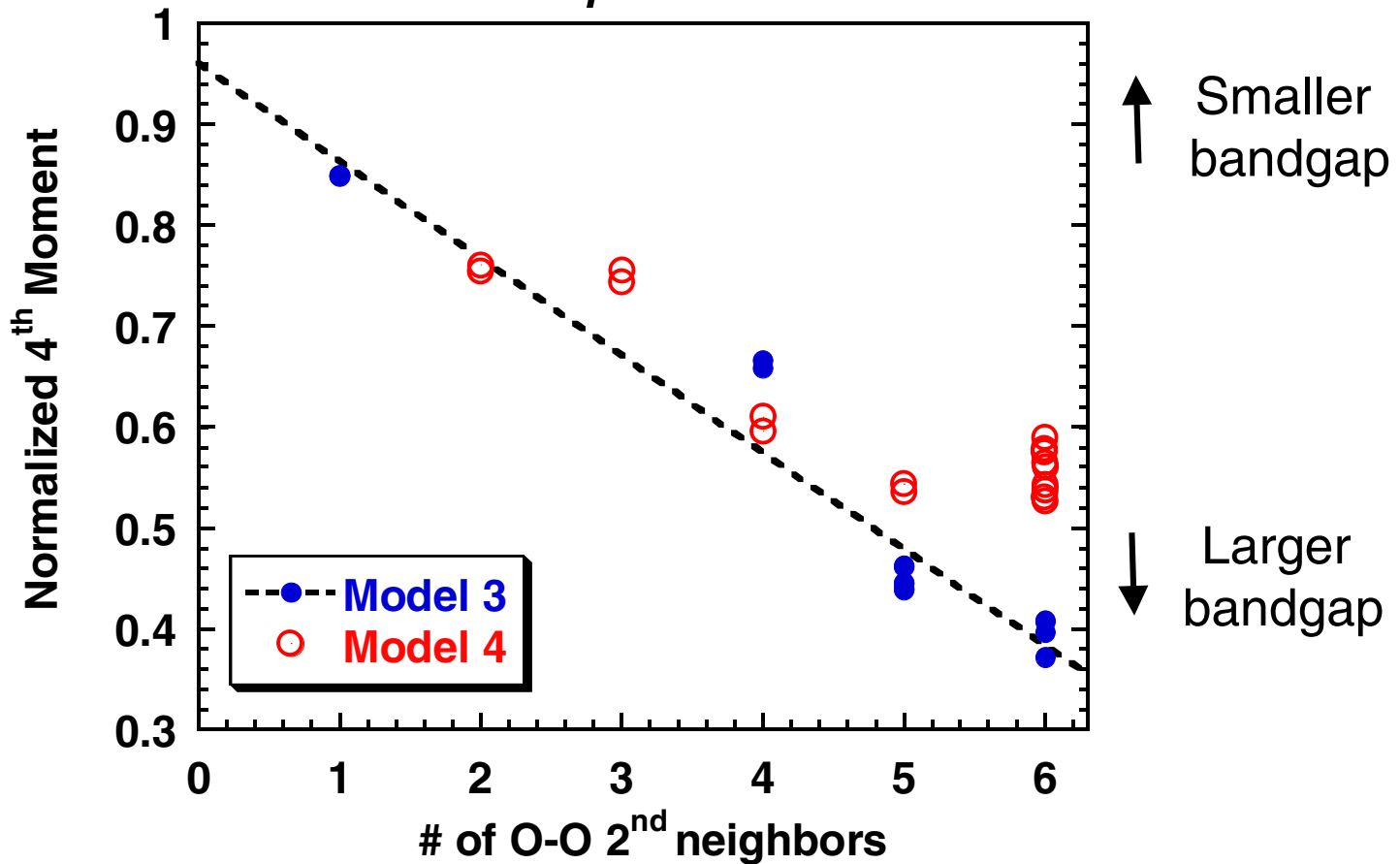
Large:
• 6 terms
• No Node on Si

➔ 4th Moment will scale with the number of O-O 2nd Neighbors

The 4th moment measures the development of the local energy-gap



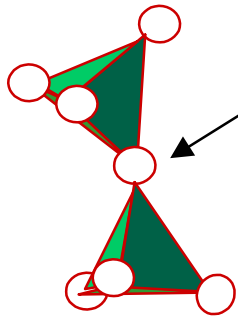
O *p*-DOS



Any Si/SiO₂ Interface must have a reduced bandgap
(last O layer has ≤ 3 O-O 2nd neighbors)

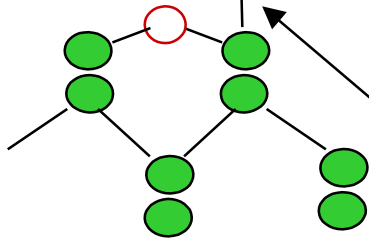
Minimum electronic interface width is 2.7 Å

Minimum Thickness for Bulk Energy Gap



For this O atom to have a bulk-like energy gap, it needs 6 O neighbors,

HOWEVER



There is a 50% bond density mismatch with Si, so the last layer of O is 1/2 concentration

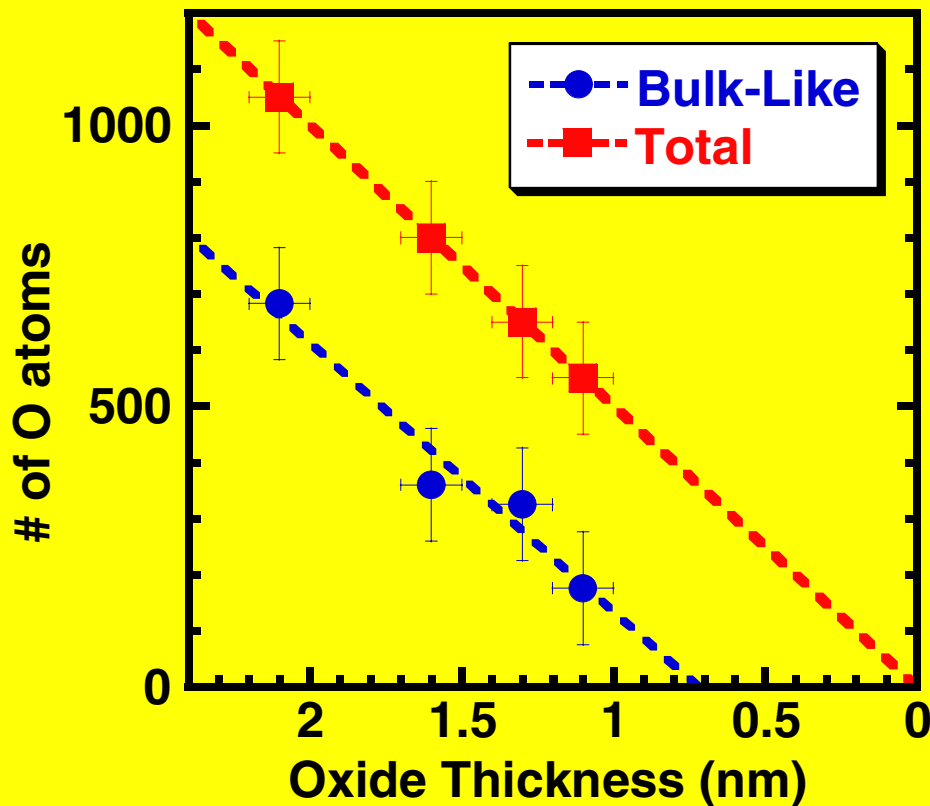
This adds another Si-O bond length (1.6 Å)

Total interface thickness is then ~ Si-O (1.6 Å) + O-O (2.7 Å)

With Si-O-Si angular distortions,

the minimum oxide thickness \approx 6.9- 8.3 Å

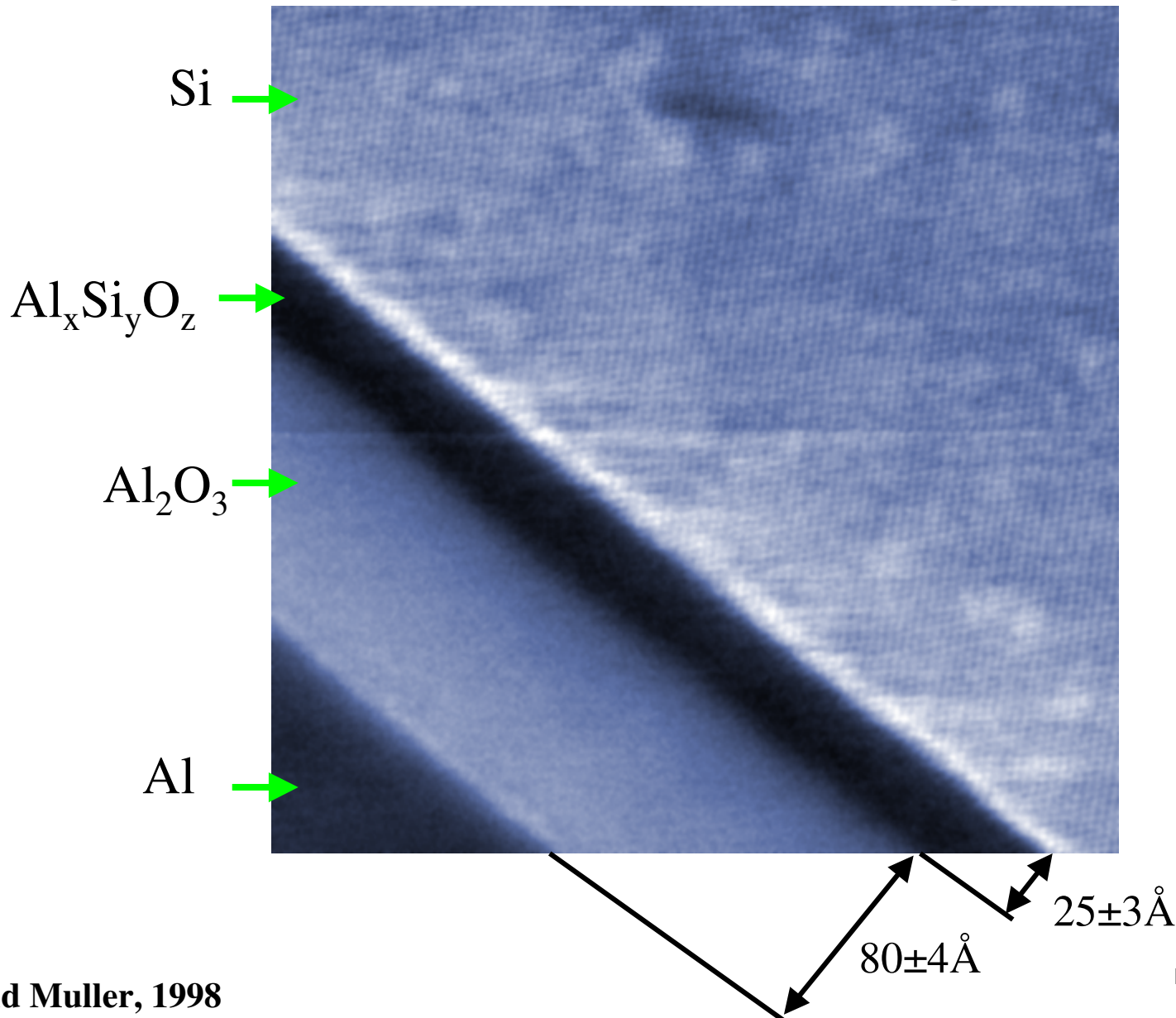
The End of the Roadmap for SiO₂



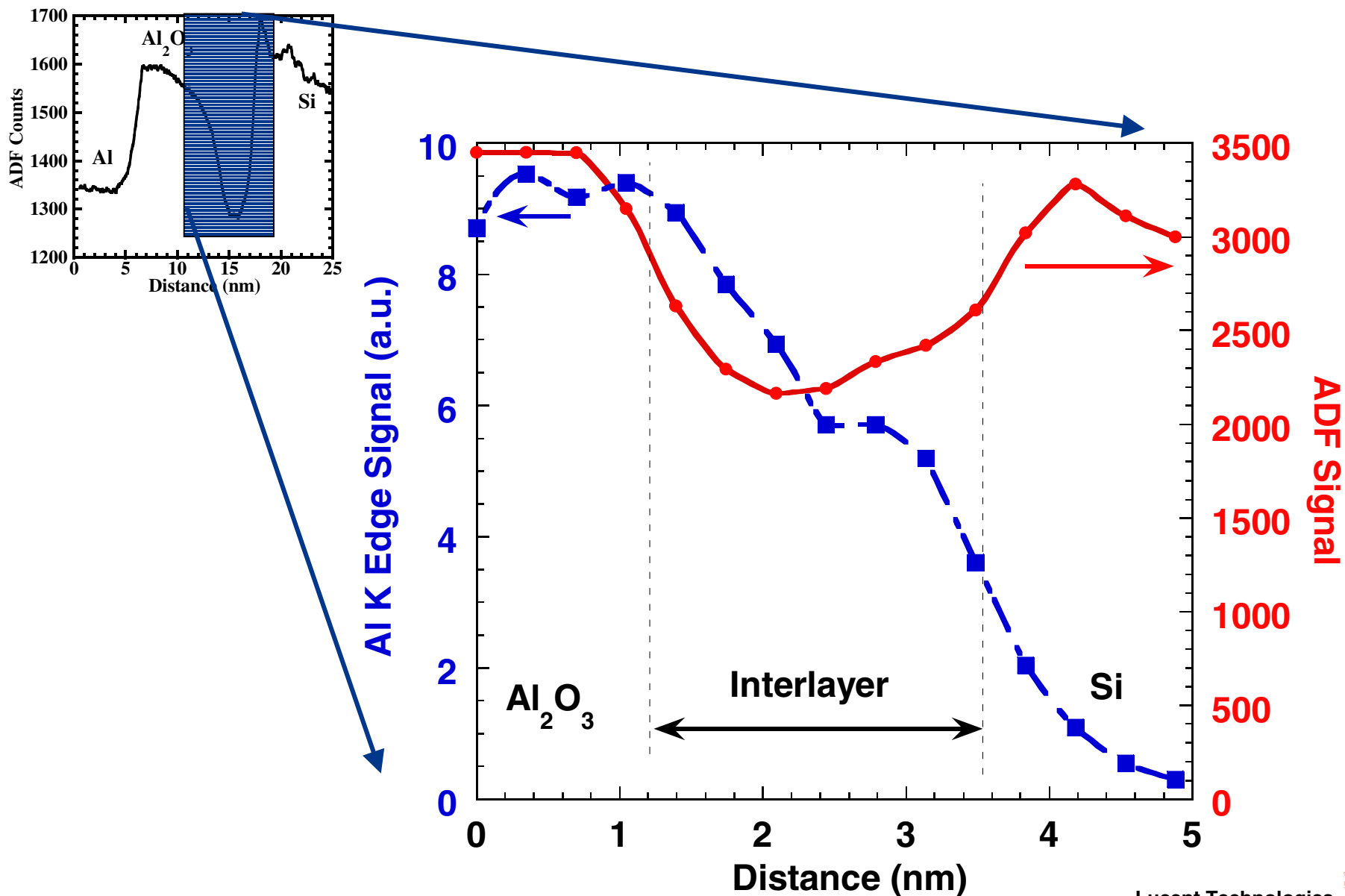
The Interface width is fixed

➔ There will be no more Bulk-like bonding when the Oxide is less than 0.7 nm.

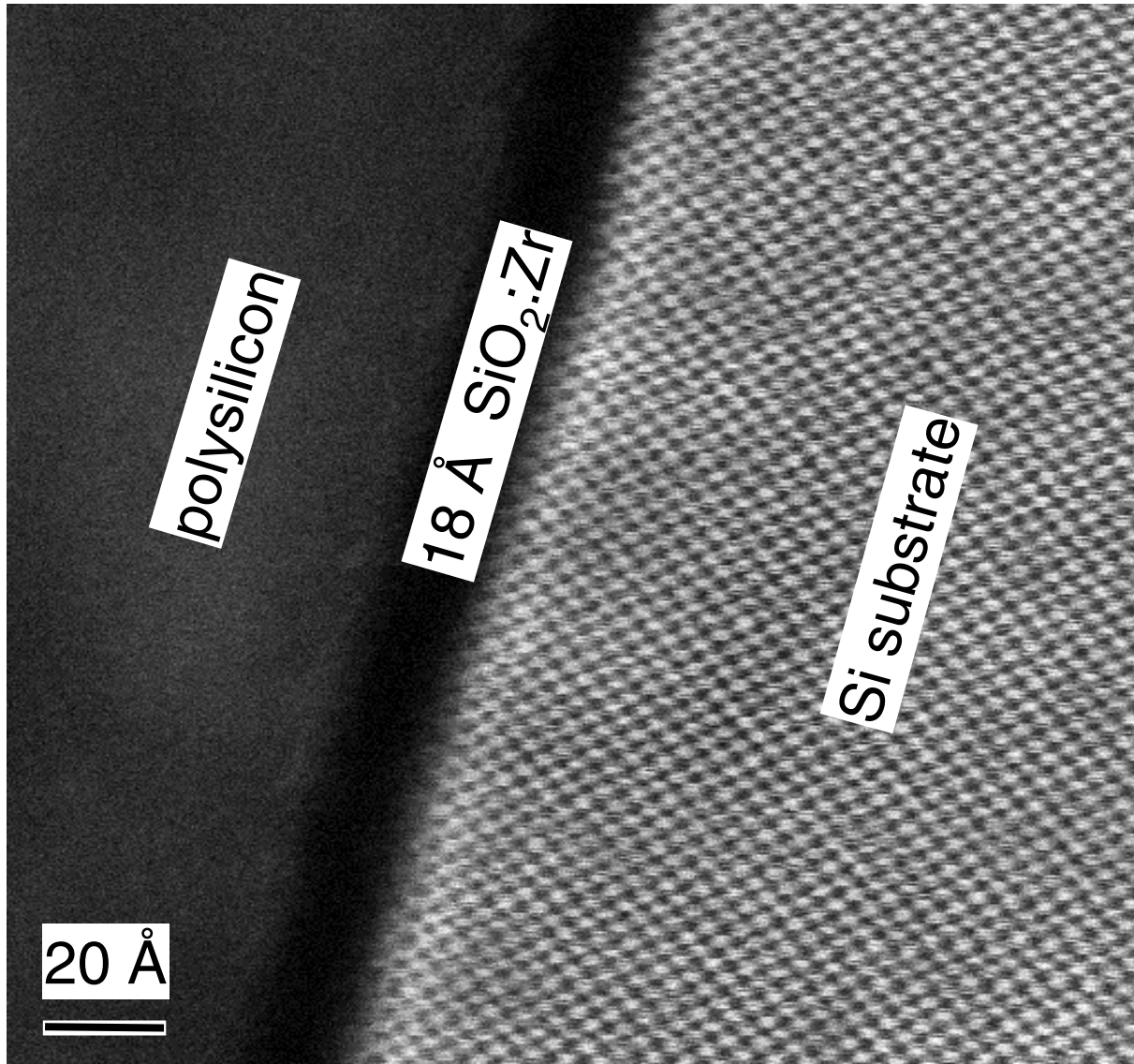
ADF Image of a Alumina Gate Oxide (Nominally 100 Å of Al_2O_3 on Si)



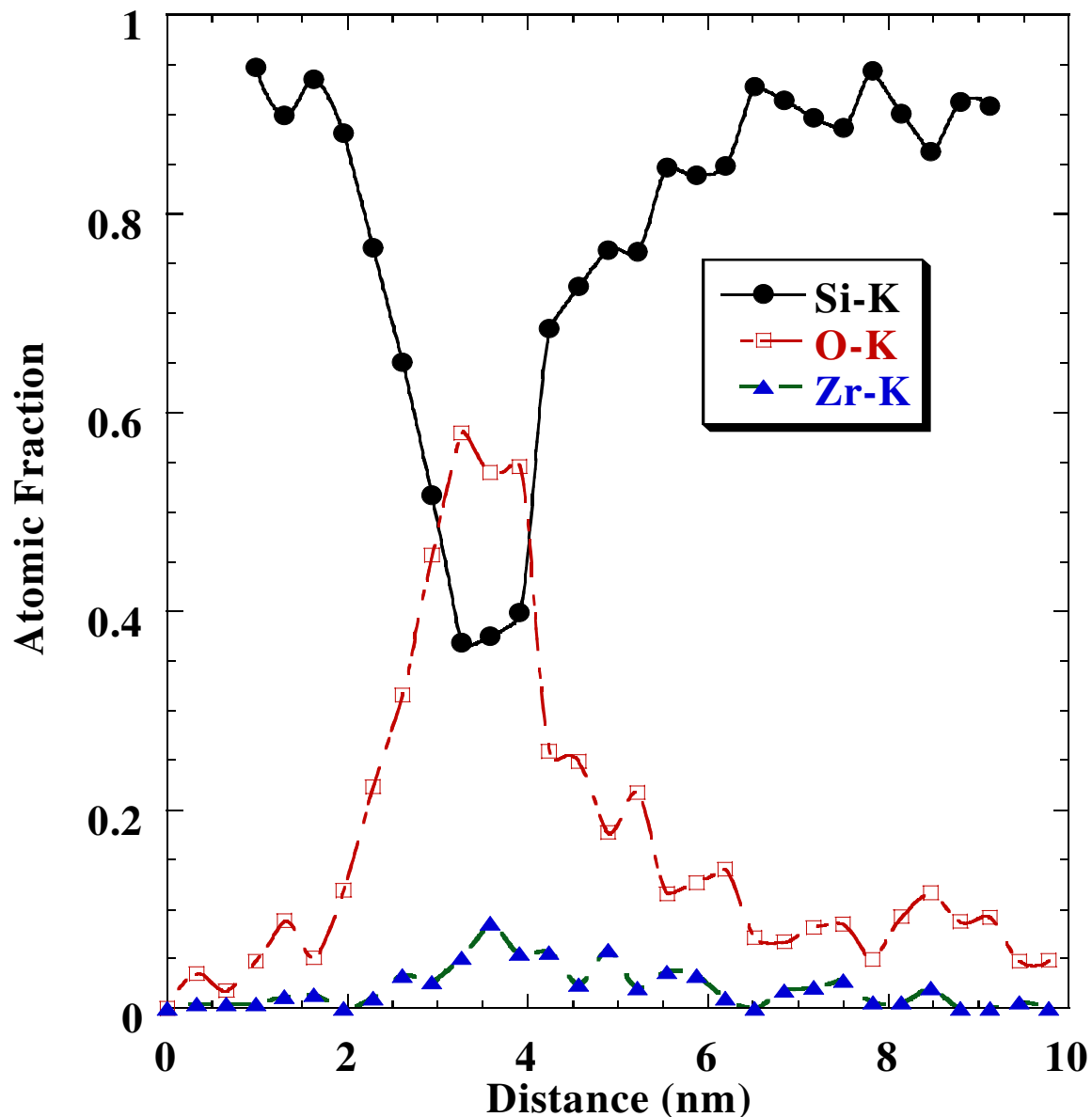
EELS Profile of the Graded $\text{Al}_x\text{Si}_y\text{O}_z$ Interlayer (Originally 100 Å of Al_2O_3 on Si)



ADF Image of the SiO₂:Zr gate Oxide (1100C)

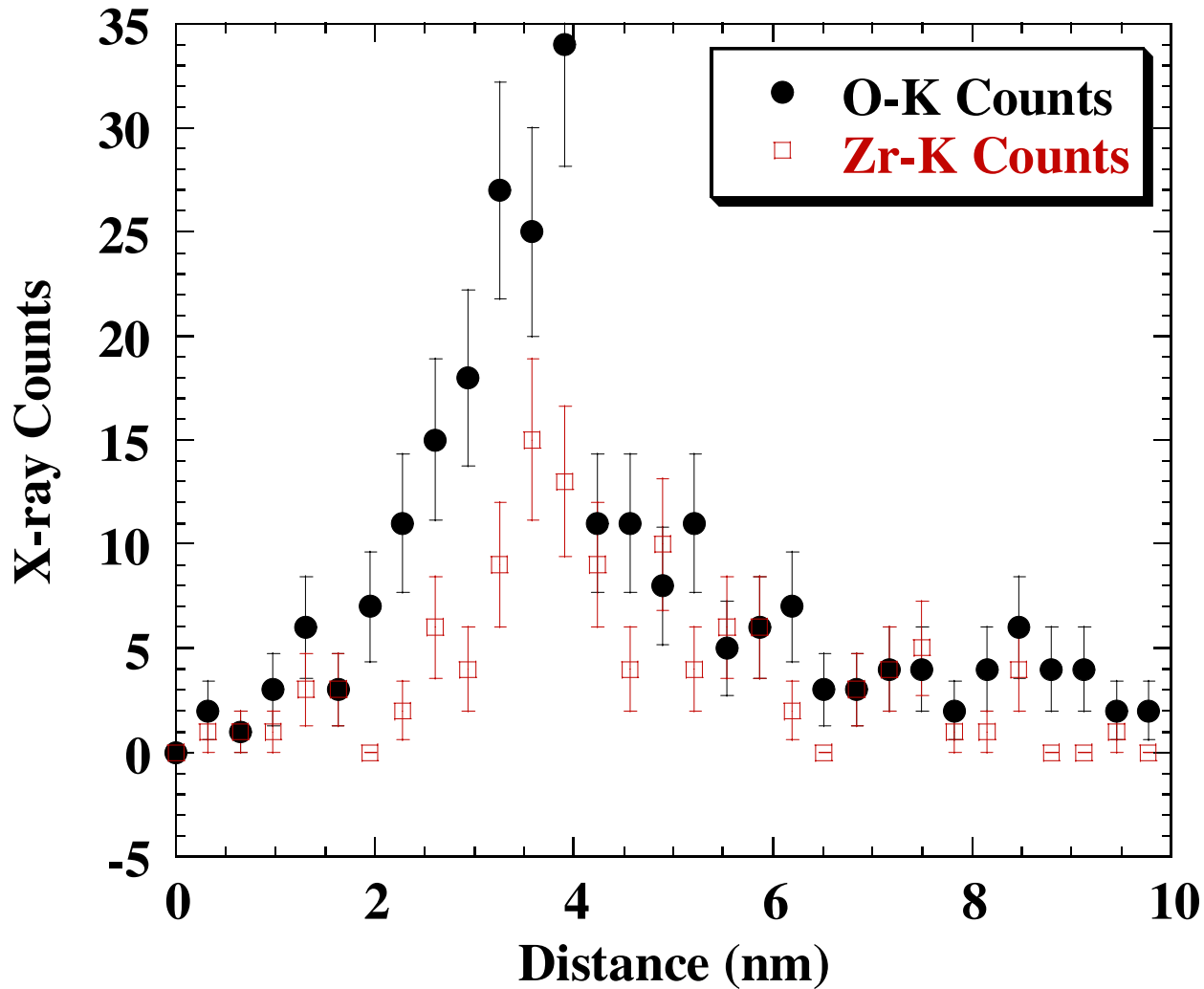


X-ray profile of the SiO₂:Zr gate Oxide



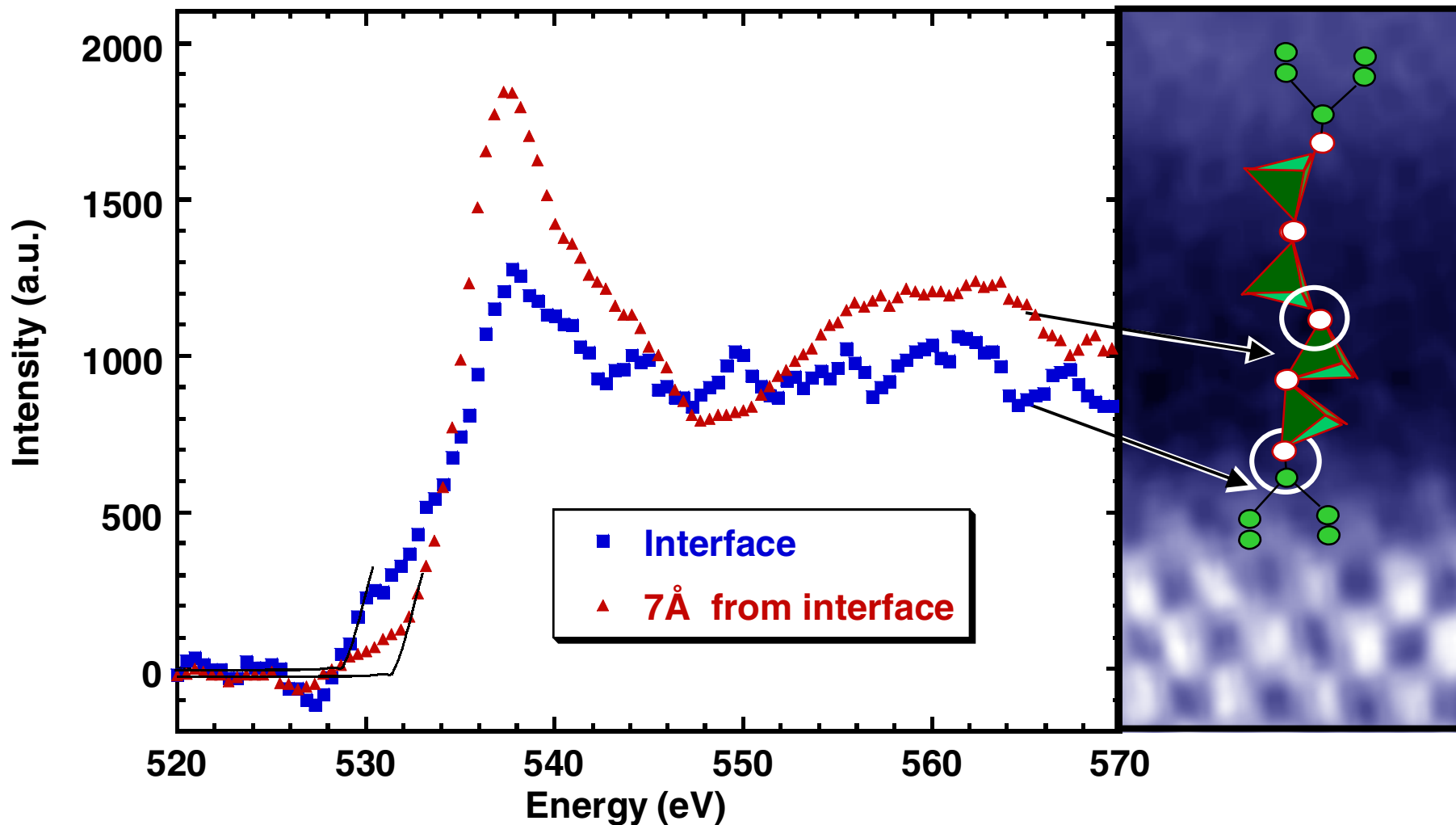
Peak Zr concentration is 8%

X-ray profile of the SiO₂:Zr gate Oxide



There may be 0.5 nm Zr-free region at the lower Si/SiO₂ interface

Oxygen Bonding at the Si/SiO₂:Zr Interface



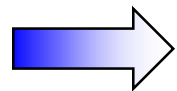
The edge onset is 3 eV lower at the interface (like SiO₂)

Gate Oxide Metrology Using STEM

- **EELS**: Chemistry and bonding at atomic resolution
Thin samples (hard to make&interpret)
- **ADF** : Chemically sensitive images at TV rates
Thick cross-sections (easy to prepare&interpret)

Physical Properties we measured (at the atomic scale):

- Correlation between local gap and O-O neighbors.
- There is an intrinsic layer ($>3 \text{ \AA}$) with a reduced bandgap at all Si/SiO₂ interfaces.



Limits how thin we can grow a gate oxide

- Interface Roughness must be controlled to take advantage of high-k Materials

