

Leading-edge Computers and the Extraordinary Research They Enable

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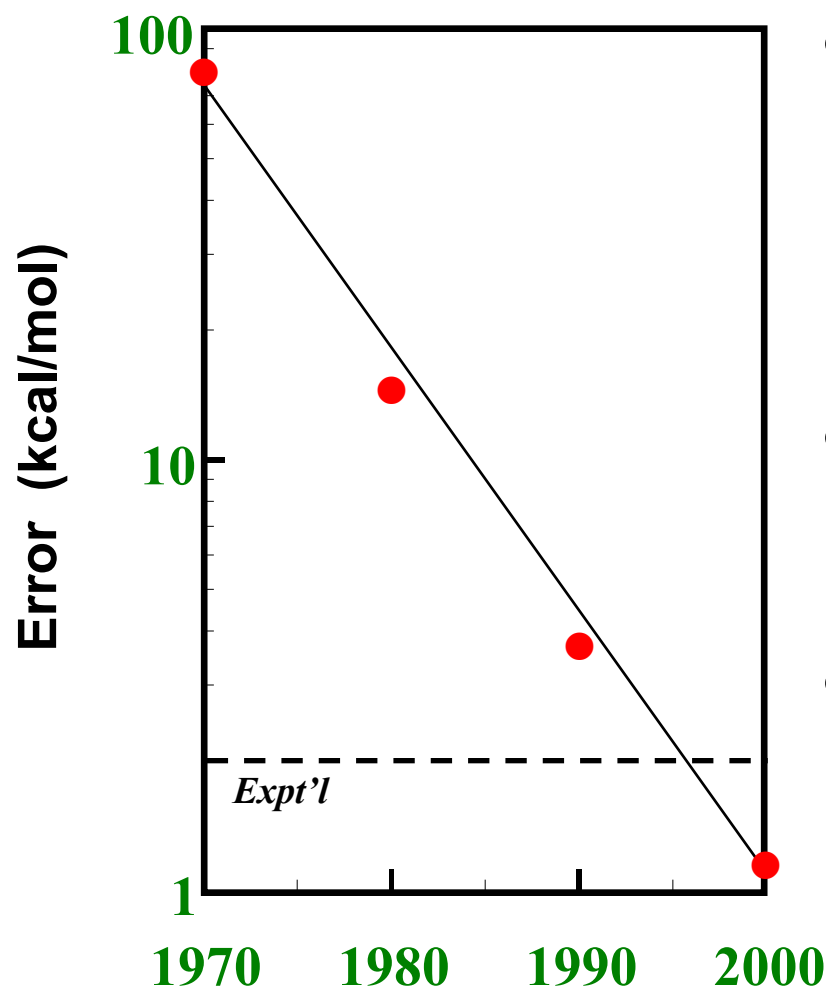
Department of Chemistry
University of Washington

More & More FLOPS & Bytes

Computational scientists always seem to need more and more computing power and storage. What is the outcome of access to increasing amounts of flops & bytes?

More & More FLOPS & Bytes

Increasing Accuracy of Predictions

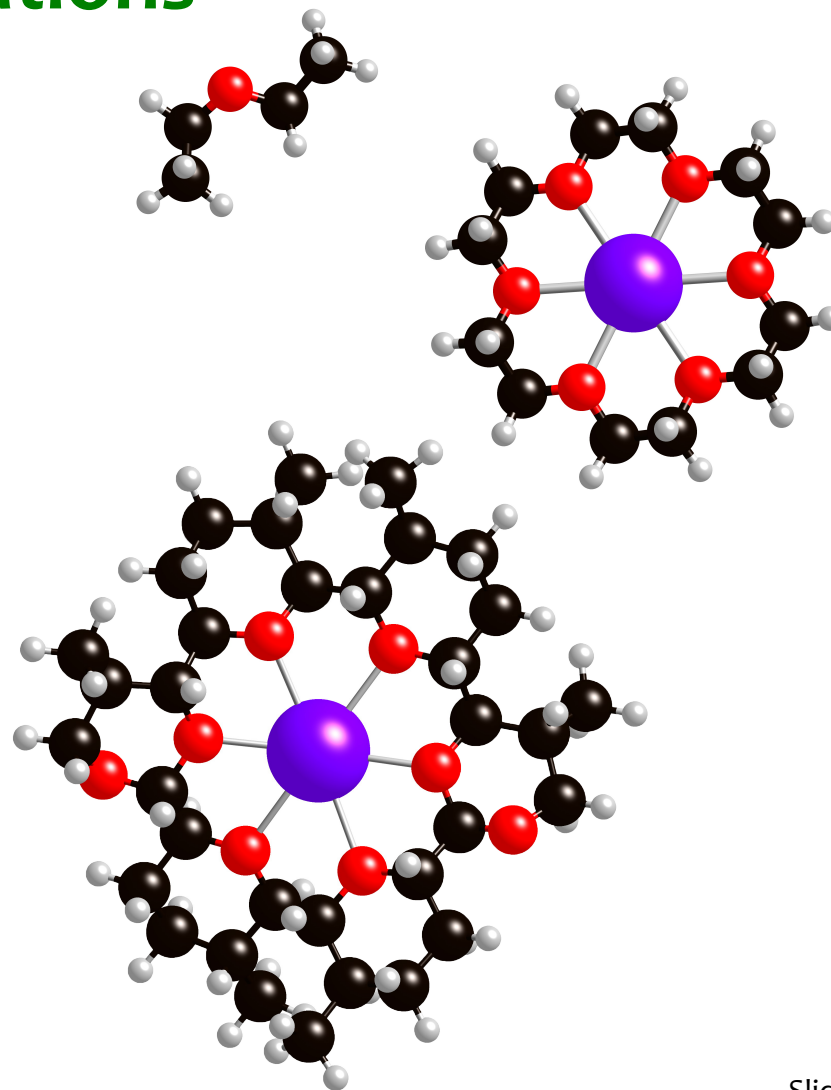


- **Bond Energies**
 - Critical for describing many chemical phenomena
 - Difficult to determine experimentally
- **Accuracy of Predictions**
 - Increased dramatically from 1970-2000
- **How?**
 - New theoretical approaches
 - New computational techniques
 - More computing power

More & More FLOPS & Bytes

Increasing reach of Simulations

- **In 1990**
 - Model systems, e.g., ether–alkali ion complexes
- **In 2000**
 - Model separations agents, e.g., 18-crown-6–alkali ion complexes
- **In 2010**
 - Real-world separations agents, e.g., Still’s crown ether–ion complexes



Every field of computational science has a similar story to tell!

The purpose of computing is insight, not numbers.

Richard W. Hamming, 1962

*The purpose of computing is numbers as well as
insight.*

with apologies to Dr. Hamming

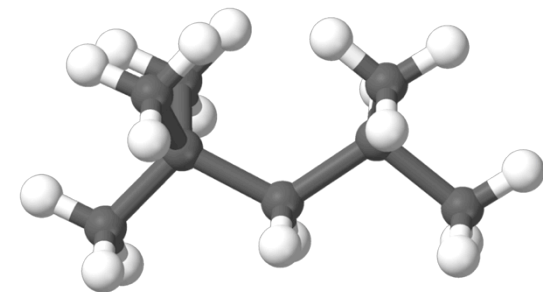
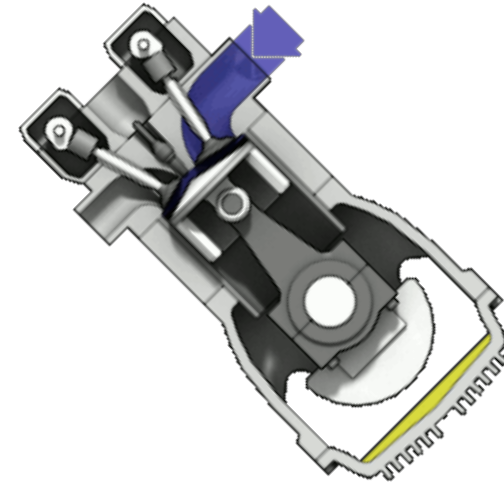
Petaflops & Petabytes

Many areas of science and engineering require extraordinary computing power to solve the mathematical equations describing the phenomena of interest and enormous data handling capability to explore the massive data sets now becoming available

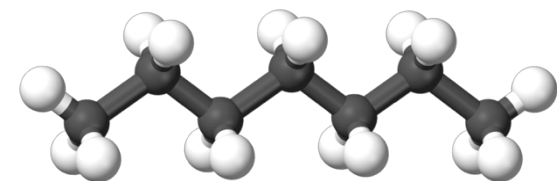
Petaflops & Petabytes

Who Needs Petaflops?

- To calculate the energy content of Iso-octane
 - Iterative solution of **275 million coupled equations**
 - Exchange of **2.5 petabytes of data** between processors
 - Exchange of **15 terabytes of data** between memory and disks
 - Execution of **30 quadrillion arithmetic operations**
- Modeling Combustion of Fuels



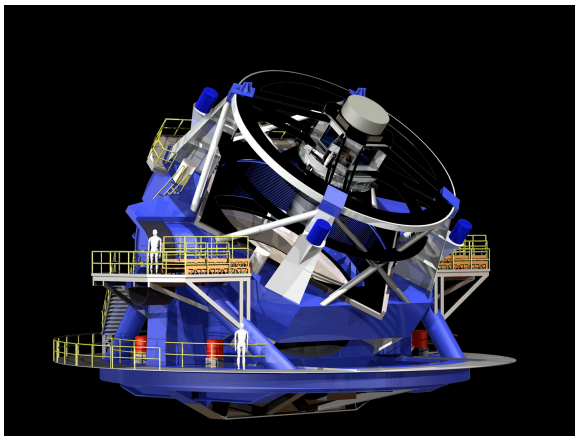
Iso-octane
(Octane Rating = 100)



n-heptane
(Octane Rating = 0)

Petaflops & Petabytes

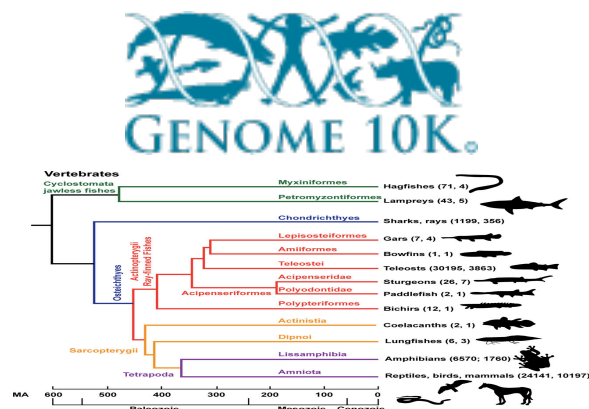
Who Needs Petabytes?



Astronomy has become one of the first digital science, replacing photographs with digital images.

*The Large Synoptic Survey Telescope (LSST) has a 3.2 gigapixel camera and will produce 15-20 terabytes of data per night and more than **100 petabytes** over its first 10 years of operation.*

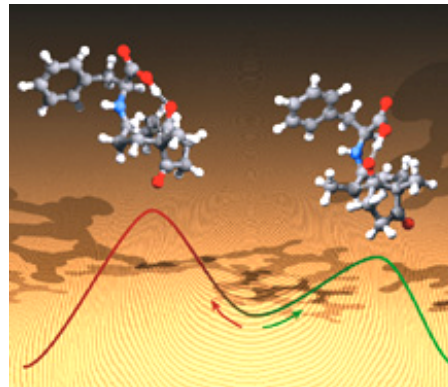
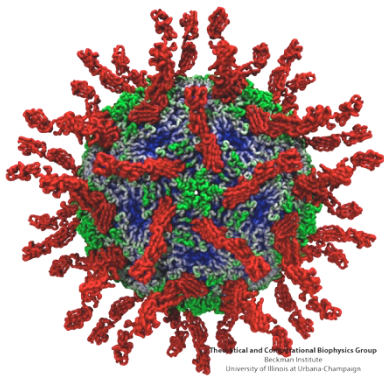
With the genomic revolution, biology and biomedicine are rapidly becoming digital sciences. The opportunities for breakthroughs in these areas are just beginning to be explored as exemplified by the Genome 10K project.



Petaflops & Petabytes

Similar Needs Across Science & Engineering

Molecular Science



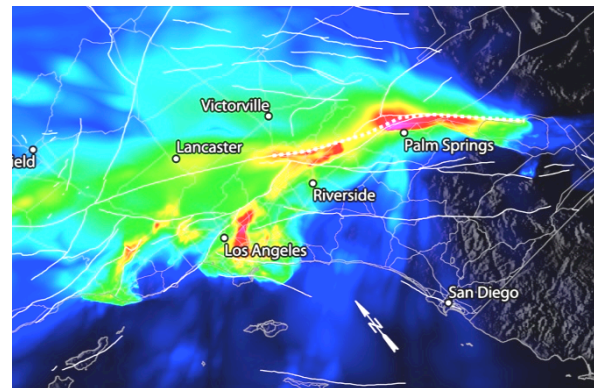
Weather & Climate



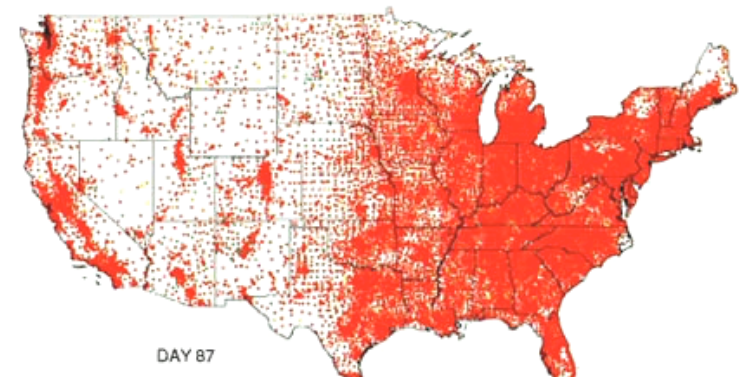
Astronomy



Geosciences



Health

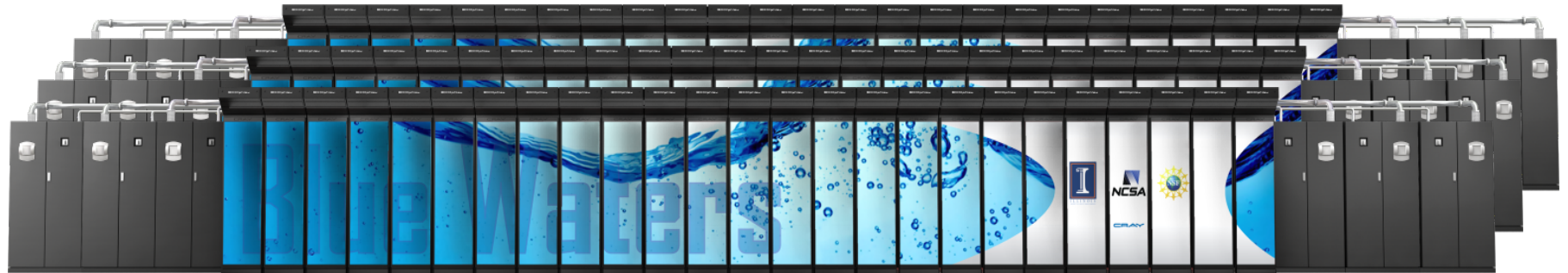


Current Leading-edge Computers: Blue Waters

Blue Waters and the National Petascale Computing Facility at the University of Illinois at Urbana-Champaign are truly extraordinary research resources for the nation.

Blue Waters

Blue Waters Computing System



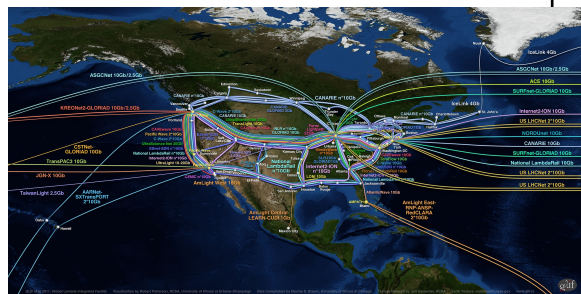
10/40/100 Gb
Ethernet Switch

IB Switch

>1 TB/sec

120+ Gb/sec

~100 GB/sec



Date: 10/2/16

WAN

Spectra Logic: 300 PBs

Sonexion: 26 PBs

Slide 11

Blue Waters

Petascale Computing Facility



Partners

EYP MCF/
Gensler
IBM
Yahoo!

- **Modern Data Center**

- 90,000+ ft² total
- 30,000 ft² raised floor
- 20,000 ft² machine room gallery

Date: 10/2/16

- **Energy Efficiency**

- LEED certified Gold
- Power Utilization Efficiency
= 1.1–1.2

Slide 12

Blue Waters

Specifications: Blue Waters & Titan

	Blue Waters	Titan
Vendor(s)	Cray/AMD/NVIDIA	
Processors	Interlagos/Kepler	Interlagos/Kepler
Peak Performance	13.1 PF	27.1 PF
CPU/GPU	7.6 / 5.5 PF	2.6 / 24.5 PF
Number of Chips (CPU/GPU)	48,352/4,224	18,688/18,688
Amount of Memory	1.66 PB	0.71 PB
Disk Storage, Capacity (usable)	26 PB	>10 TB
Disk Storage, Bandwidth (sustained)	1.2 TB/s	0.24 TB/s
Archival Storage, Capacity (usable)	300 PB	125 PB
Archival Storage, Bandwidth (sustained)	88 GB/s	18 GB/s

Blue Waters

Specifications: Blue Waters & Titan

	Blue Waters	Titan
Vendor(s)	Cray	IBM
Processors	Cray T3E/AM2/NVIDIA Interlagos/Kepler	Interlagos/Kepler
Peak Performance	13.5 PF	27.1 PF
CPU/GPU	7.6 / 5.5	2.6 / 24.5
Number of Cores (CPU/GPU)	48,352/4,224	18,688/18,688
Amount of Memory	1.66 PB	0.71 PB
Disk Storage, Capacity (usable)	26 PB	>10 TB
Disk Storage, Bandwidth (sustained)	1.2 TB/s	0.24 TB/s
Archival Storage, Capacity (usable)	300 PB	125 PB
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A CAUTIONARY NOTE:
SUSTAINED PERFORMANCE:
BLUE WATERS: 1.37 PFS
TITAN: 0.64 PFS

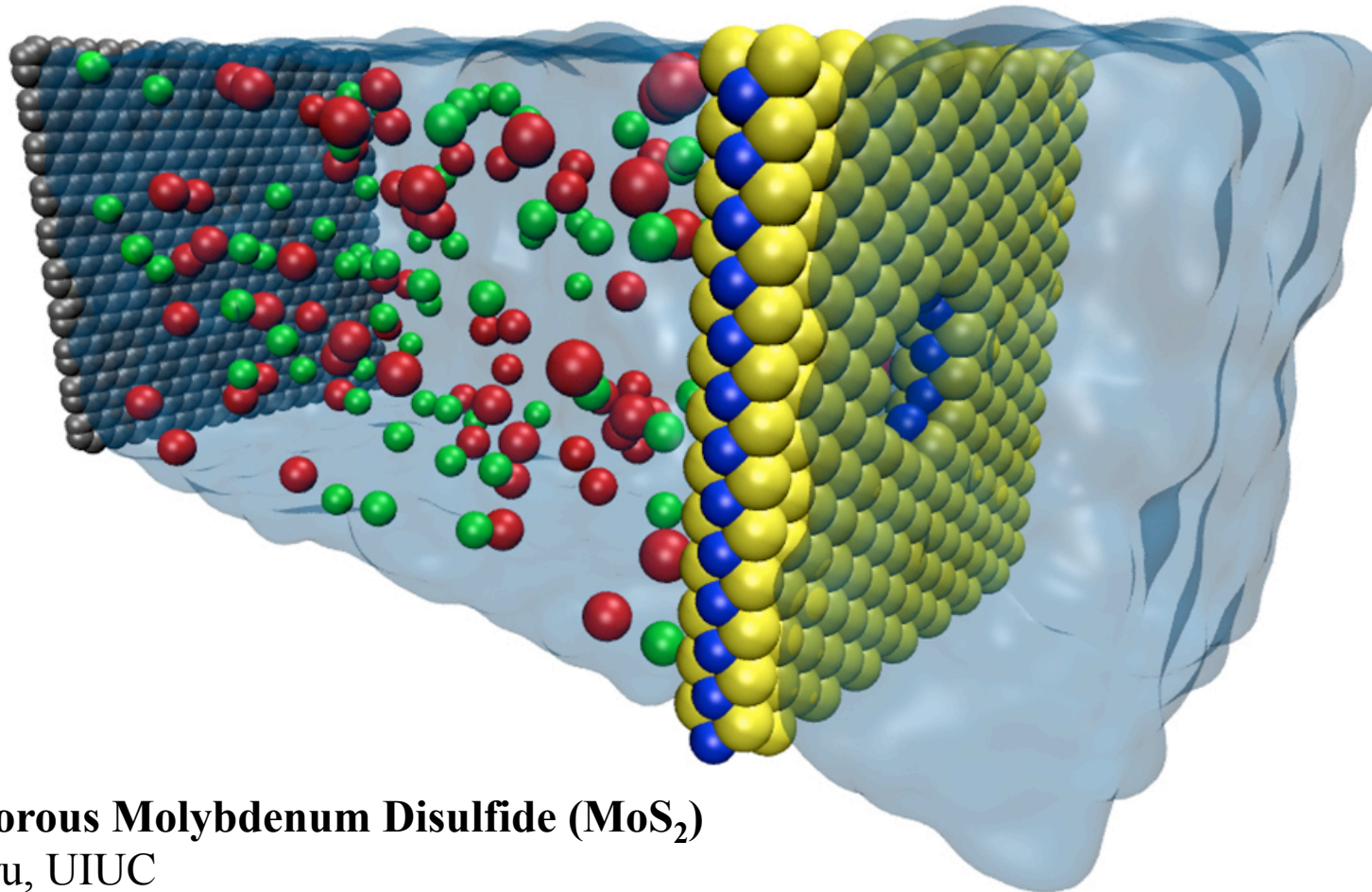
Blue Waters

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Blue Waters

Exploring New Materials for Desalination

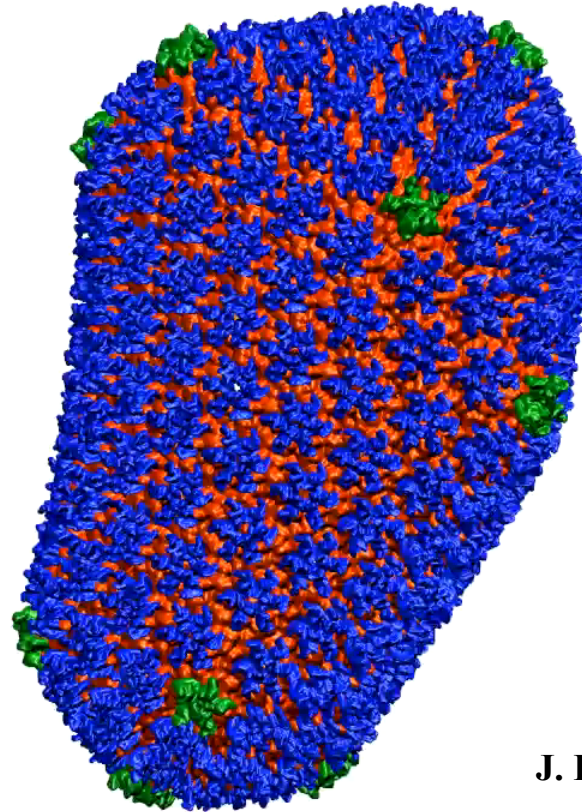


Nanoporous Molybdenum Disulfide (MoS₂)

N. Aluru, UIUC

Blue Waters

Modeling the HIV-1 Capsid



**J. Perilla, G. Zhao, A. Gronenborn,
P. Zhang and Klaus Schulten**

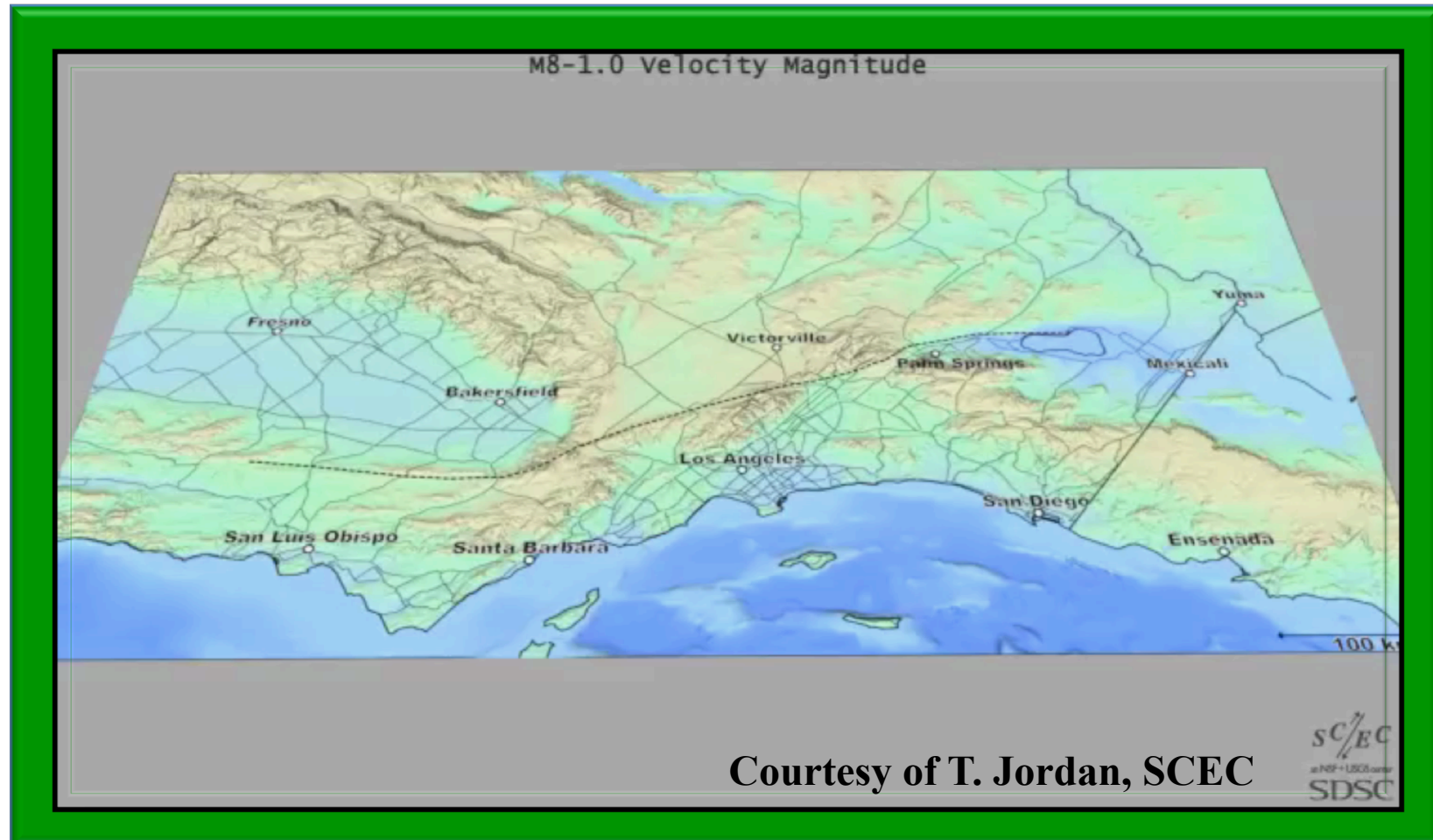
Blue Waters

Predicting the Impact of Earthquakes

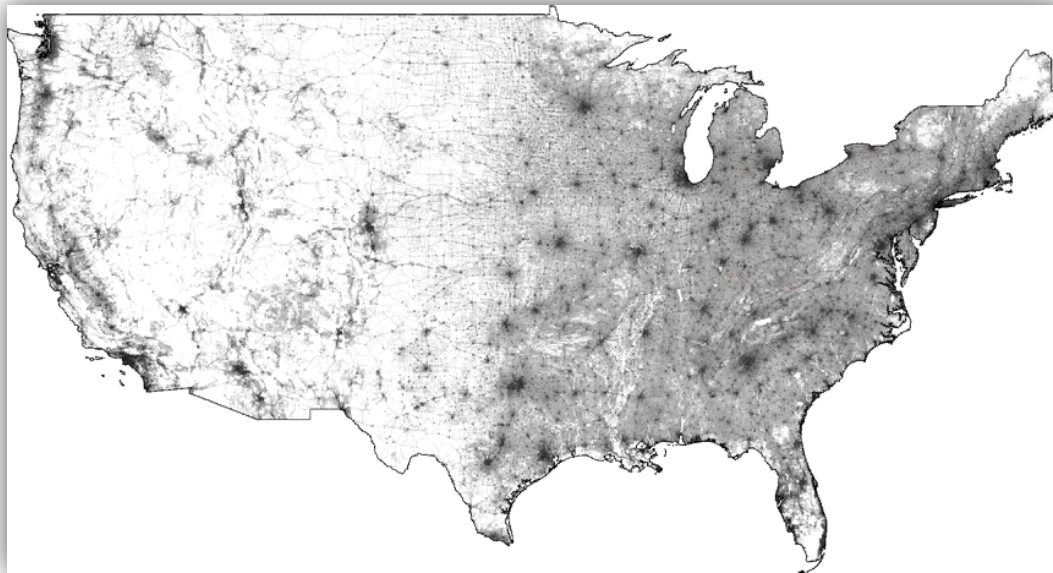


Blue Waters

One of Many Earthquake Scenarios



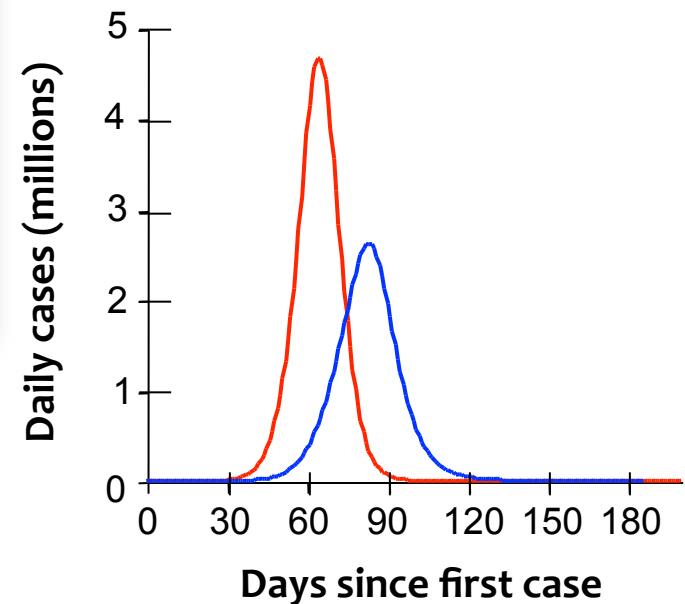
Spread of Contagious Diseases



Video courtesy of N. Ferguson,
Imperial College, London

No Intervention

Intervention: next-day treatment of 90% of cases with anti-virals, school closures, 50% household quarantine.



Future Leading-edge Computers: Summit & Aurora

Although computing technology continues to advance, the best strategy for architecting exascale computers is unclear—the U.S. Department of Energy has decided that two different architectures will be explored.

Future Leading-edge Supercomputers

Oak Ridge's Summit & Argonne's Aurora Systems

	Summit (2018)	Aurora (2018)
Processor	IBM Power9/NVIDIA Volta	Intel Knights Hill
Peak Performance	>150 PF	180 PF
Cores/Processor	Up to 24	>72
Number of Nodes	~3,400	>50,000
Memory	>1.7 PB	>7 PB
Interconnect BS Bandwidth	?	>500 TB/s
File System Capacity	~120 PB	>150 PB
File System Bandwidth	~1 TB/s	>1 TB/s
Peak Power	~ 10 MW	13 MW

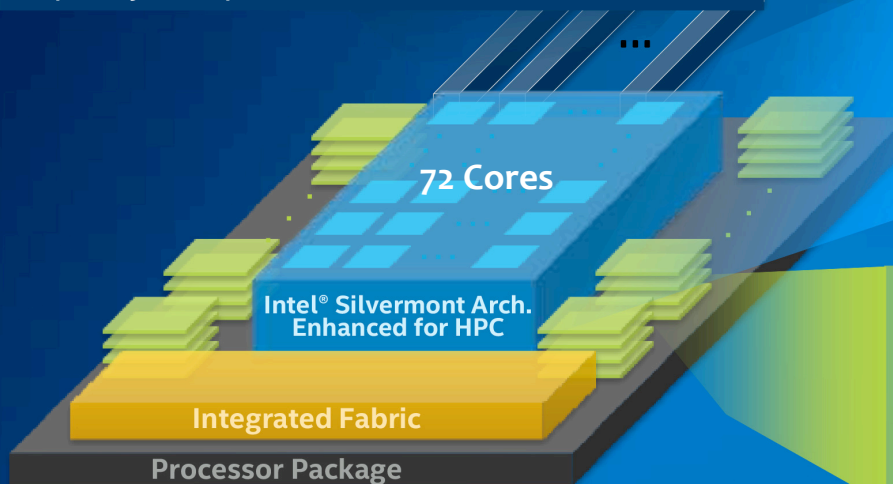
Future Leading-edge Supercomputers

Intel's Many Integrated Core (MIC) Processors

Unveiling Details of Knights Landing

(Next Generation Intel® Xeon Phi™ Products)

Platform Memory: DDR4 Bandwidth and Capacity Comparable to Intel® Xeon® Processors



★ 2nd half '15
1st commercial systems

★ 3+ TFLOPS¹
In One Package
Parallel Performance & Density

Compute: Energy-efficient IA cores²

- Microarchitecture enhanced for HPC³
- 3X Single Thread Performance vs Knights Corner⁴
- Intel Xeon Processor Binary Compatible⁵

On-Package Memory:

- up to 16GB at launch
- 1/3X the Space⁶
- 5X Bandwidth vs DDR4⁷
- 5X Power Efficiency⁶

Jointly Developed with Micron Technology

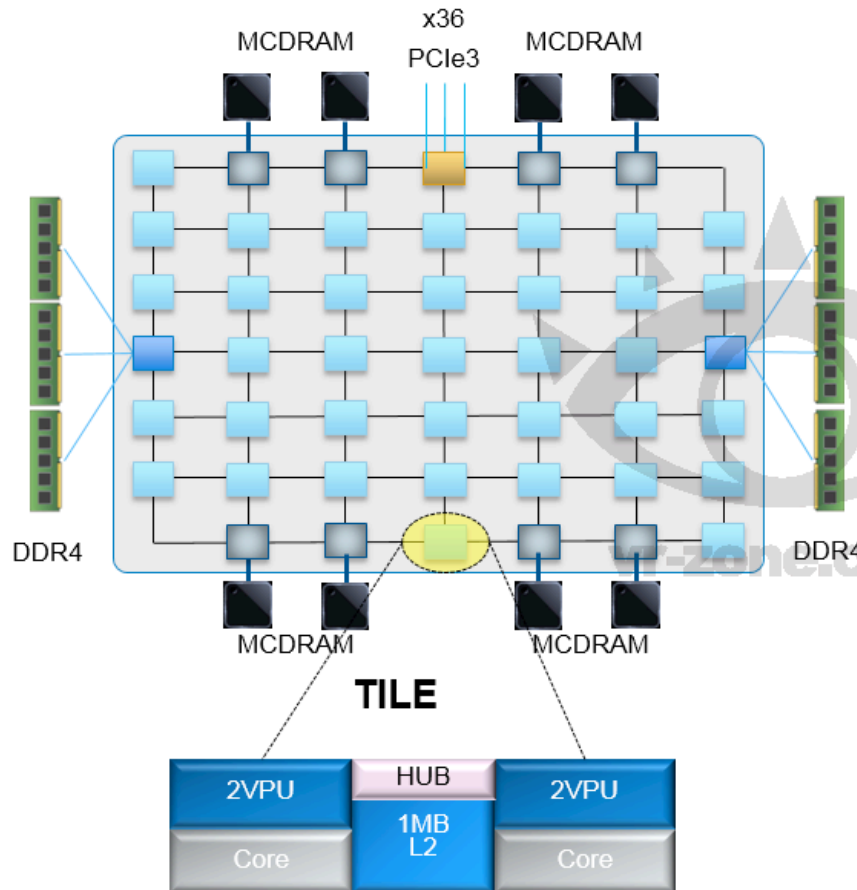
All products, computer systems, dates and figures specified are preliminary based on current expectations, and are subject to change without notice. ¹Over 3 Teraflops of peak theoretical double-precision performance is preliminary and based on current expectations of cores, clock frequency and floating point operations per cycle. FLOPS = cores x clock frequency x floating-point operations per second per cycle. ²Modified version of Intel® Silvermont microarchitecture currently found in Intel® Atom™ processors. ³Modifications include AVX512 and 4 threads/core support. ⁴Projected peak theoretical single-thread performance relative to 1st Generation Intel® Xeon Phi™ Coprocessor 7120P (formerly codenamed Knights Corner). ⁵Binary Compatible with Intel Xeon processors using Haswell Instruction Set (except TSX). ⁶Projected results based on internal Intel analysis of Knights Landing memory vs Knights Corner (GDDR5). ⁷Projected result: based on internal Intel analysis of STREAM benchmark using a Knights Landing processor with 16GB of ultra high-bandwidth versus DDR4 memory only with all channels populated.



Conceptual—Not Actual Package Layout

Future Leading-edge Supercomputers

Knights Landing Processor Architecture



Up to 72 Intel Architecture cores based on Silvermont (Intel® Atom processor)

- Four threads/core
- Two 512b vector units/core
- Up to 3x single thread performance improvement over KNC generation

Full Intel® Xeon processor ISA compatibility through AVX-512 (except TSX)

6 channels of DDR4 2400 MHz -up to 384GB

36 lanes PCI Express* Gen 3

8/16GB of high-bandwidth on-package MCDRAM memory >500GB/sec

200W TDP

Future Leading-edge Supercomputers

Knights Hill Processor


User Upgrade Program Available TODAY

Intel® Xeon Phi™ Product Family

Industry and User Momentum

1 TFLOPS¹

Knights Corner




[Intel® Xeon Phi™ Coprocessor – Applications and Solutions Catalog](#)


3+ TFLOPS²

- Bootable Processor
- On-Pkg, High BW Memory
- Integrated Fabric

Knights Landing

2H'15 First Commercial Systems



+ 

>50 systems providers expected³

many more card-based systems

>100 PFLOPS customer system compute commits to-date³

Announcing


Knights Hill

3rd Generation Intel® Xeon Phi™ Product Family

2nd Generation Intel Omni-Path Architecture

10nm process technology

¹ Claim based on calculated theoretical peak double precision performance capability for a single coprocessor. 16 DP FLOPS/clock/core * 61 cores * 1.23GHz = 1.208 TeraFLOPS
² Over 3 Teraflops of peak theoretical double-precision performance is preliminary and based on current expectations of cores, clock frequency and floating point operations per cycle.
FLOPS = cores x clock frequency x floating-point operations per second per cycle. ³ Intel internal estimate



Future Leading-edge Supercomputers

International Efforts

- China
 - Dramatic push in supercomputing
 - Now has 1/3-rd of the supercomputers on Top500
 - Building domestic HPC ecosystem (hardware, software, systems)
 - Continuing series of #1 systems being built by China
 - 2011: Tianhe-1A (4.7 PFs); 2013: Tianhe-2 (55 PFs); 2016: Sunway (125 PFs); ...
- Japan
 - Flagship 2020 Project: Post-K computer development
 - RIKEN AICS with Fujitsu + ARM (with HPC extensions)
 - Target: 50-100x K computer

Future Leading-edge Supercomputers

International Efforts

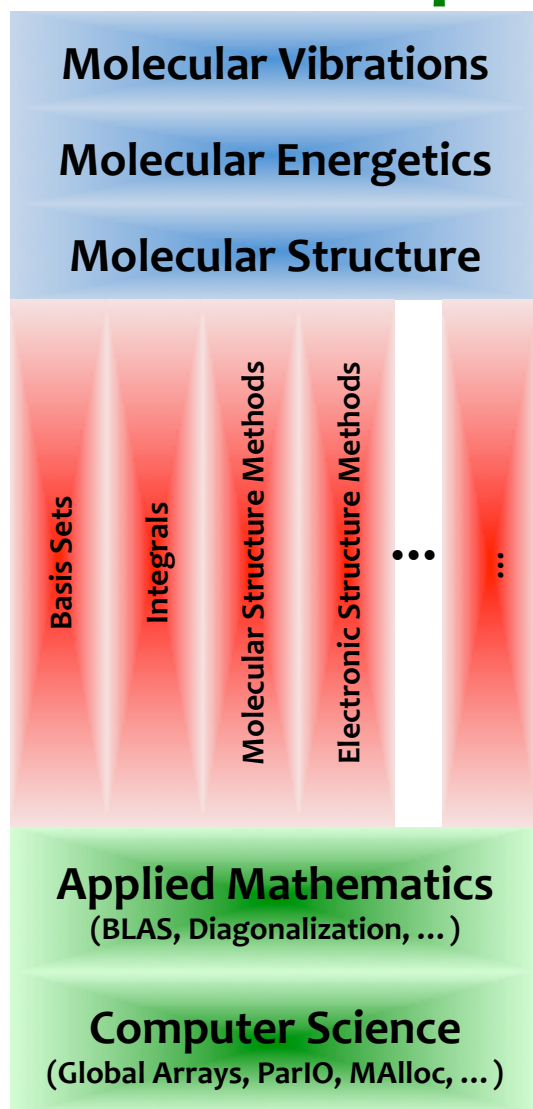
- Europe
 - PRACE
 - 25 members, 2 observers
 - Major efforts in Spain, France, Germany, Italy

Supercomputing Applications

The true value of any computing technology is measured by the applications that it enables. As we saw in comparing Blue Waters & Titan, enabling applications to take full advantage of new computing technologies is a challenging task.

Supercomputing Applications

NWChem: An Exemplary SC Application



**Deep collaboration
between
computational
chemists, applied
mathematicians, and
computer scientists**

Supercomputing Applications

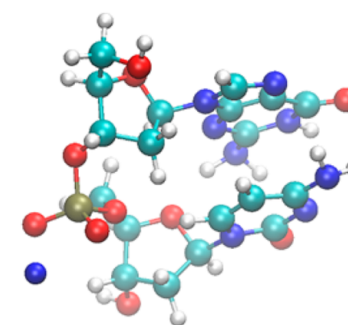
Performance: Another Cautionary Tale

NWChem can achieve impressive performance on petascale computers for the most flop-intensive calculations. For example, for CCSD(T) calculations, which is the current “gold” standard in electronic structure theory, this is the (T) algorithm:

Method	Time(s)*	GFLOP Count	PF/s
CCSD			
(T)	5024	5,948,249,197	1.18
CCSD(T)			

* On 20,000 XE6 nodes (Blue Waters)

V. M. Anisimov, G. H. Bauer, K. Chadalavada, R. M. Olson, J. Glenski, W. T. C. Kramer, E. Aprà, and K. Kowalski, *J. Chem. Theory Comput.* **10**, 4307-4316 (2014).



guanine- cytosine
deoxydinucleotide
monophosphate + Na⁺

However, this is only part of the story. One needs the CCSD amplitudes for the (T) algorithm. The CCSD algorithm is far more complex with a much higher communication/compute ratio than the (T) algorithm:

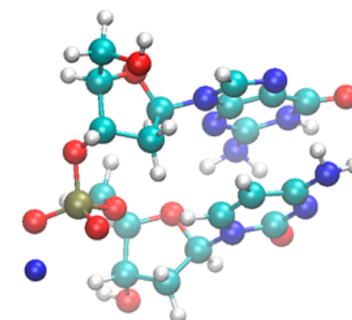
Supercomputing Applications

Performance: Another Cautionary Tale II

Method	Time(s)*	GFLOP Count	PF/s
CCSD	14,406	195,796,351	0.01
(T)	5024	5,948,249,197	1.18
CCSD(T)	19,430	6,144,045,548	0.32

* On 20,000 XE6 nodes (Blue Waters)

V. M. Anisimov, G. H. Bauer, K. Chadalavada, R. M. Olson, J. Glenski, W. T. C. Kramer, E. Aprà, and K. Kowalski, *J. Chem. Theory Comput.* **10**, 4307-4316 (2014).



guanine- cytosine
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monophosphate + Na⁺

So, the CCSD algorithm consumes $\frac{3}{4}$ -th of the time. Further, the algorithm uses a substantial amount of memory, duplicating arrays to minimize communication costs, which limits the number of cores/node that can be used—just 1 of 16 cores on a Blue Waters node that has 64 GBs of memory on the node.

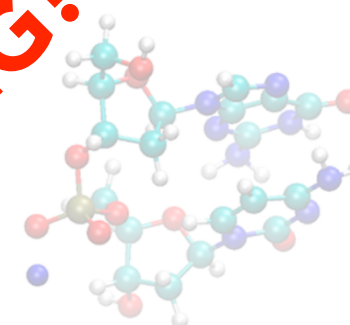
Supercomputing Applications

Performance of NWChem on Blue Waters II

Method	Time(s)*	GFLOP Count	MF/s
CCSD	14,406	195,796,351	0.01
(T)	5024	5,948,249,107	0.18
CCSD(T)	19,430	6,144,045,348	0.32

* On 20,000 XE6 nodes (Blue Waters)

V. M. Anisimov, G. H. Bauer, K. Chadalavada, R. M. Olson, J. Glenski, W. T. C. Kramer, E. Aprà, and K. Kowalski, *J. Chem. Theory Comput.* 10, 4307-4316 (2014).



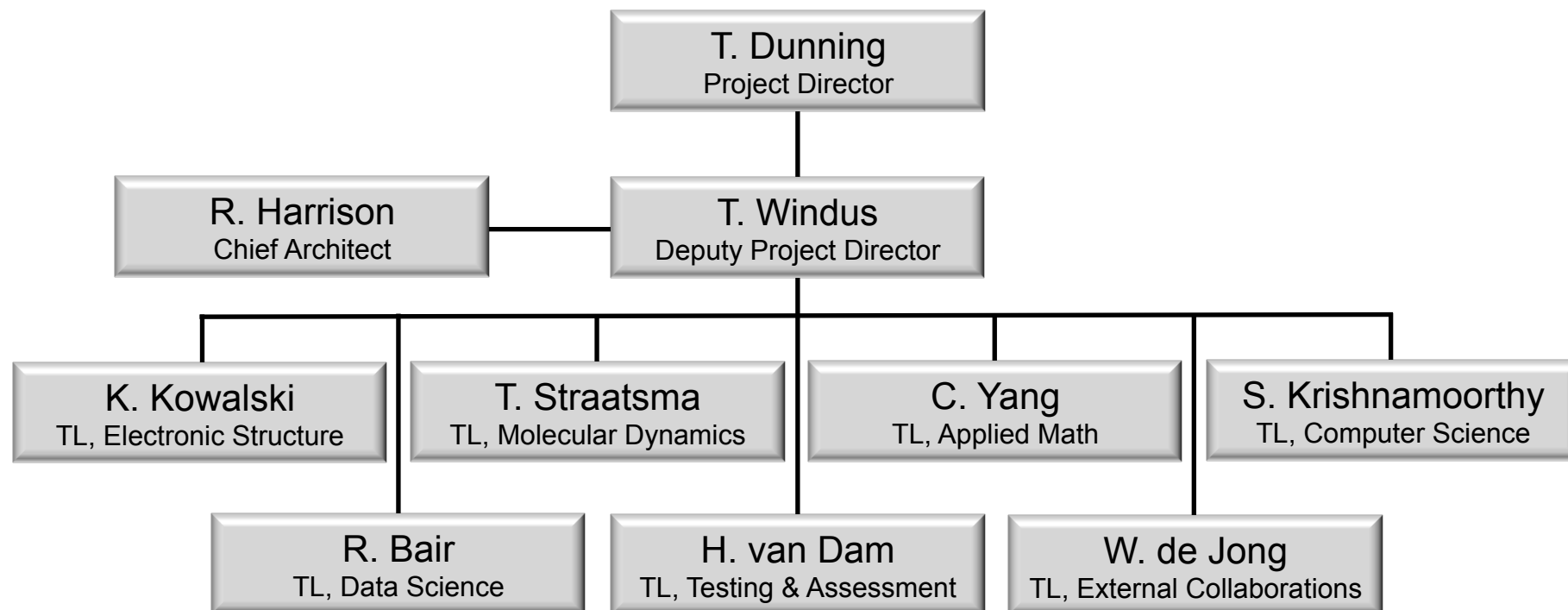
guanine- cytosine
deoxydinucleotide
monophosphate + Na⁺

So, the CCSD algorithm consumes 1/4-th of the time. Further, the algorithm uses a substantial amount of memory, duplicating arrays to minimize communication costs, which limits the number of cores/node that can be used—just 1 of 16 cores on a Blue Waters node that has 64 GBs of memory on the node.

THIS IS THE CHALLENGE OF EXASCALE COMPUTING!

Supercomputing Applications

NWChemEx ECP Project



- Redesign NWChem to create a more modular and library-oriented framework
- Incorporate new mathematical algorithms to reduce complexity and improve scalability
- Incorporate new computer science approaches and technologies to reduce/more effectively use memory, separate the details of the hardware from the software
- Identify, assess, and implement new developments in computational chemistry

Thoughts on What Needs to be Done

As computing technology continues to advance—and change—how do we ensure that computational science and engineering continues to be able to take advantage of these advances?

From Here to the Future

What is Needed to Continue to Advance

- **Better Understanding of Software-Hardware Interface**
 - Current benchmarks do not represent full range of applications
 - Current benchmarks provide little information on programmability
- **New Algorithms**
 - To fully exploit increasing concurrency
 - That are adapted to:
 - Decreased memory per flop
 - Decreased interconnect bandwidth per flop
 - Decreased I/O bandwidth per flop
- **New Programming Models**
 - Need better programming languages, domain specific languages
- **Better Education and Training**



Thank You!